

# VAMOS: Middleware for Best-Effort Third-Party Monitoring

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**Abstract.** As the complexity and criticality of software increase every year, so does the importance of run-time monitoring. Third-party monitoring, with limited knowledge of the monitored software, and best-effort monitoring, which keeps pace with the monitored software, are especially valuable, yet underexplored areas of run-time monitoring. Most existing monitoring frameworks do not support their combination because they either require access to the monitored code for instrumentation purposes or the processing of all observed events, or both.

We present a middleware framework, VAMOS, for the run-time monitoring of software which is explicitly designed to support third-party and best-effort scenarios. The design goals of VAMOS are (i) efficiency (keeping pace at low overhead), (ii) flexibility (the ability to monitor black-box code through a variety of different event channels, and the connectability to monitors written in different specification languages), and (iii) ease-of-use. To achieve its goals, VAMOS combines aspects of event broker and event recognition systems with aspects of stream processing systems.

We implemented a prototype toolchain for VAMOS and conducted experiments including a case study of monitoring for data races. The results indicate that VAMOS enables writing useful yet efficient monitors, is compatible with a variety of event sources and monitor specifications, and simplifies key aspects of setting up a monitoring system from scratch.

## 1 Introduction

Monitoring—the run-time checking of a formal specification—is a lightweight verification technique for deployed software. Writing monitors is especially challenging if it is *third-party* and *real-time*. In third-party monitoring, the monitored software and the monitoring software are written independently, in order to increase trust in the monitor. In the extreme case, the monitor has very limited knowledge of and access to the monitored software, as in black-box monitoring. In real-time monitoring, the monitor must not slow down the monitored software while also following its execution close in time. In the extreme case, the monitor may not be able to process all observed events and can check the specification only approximately, as in best-effort monitoring.

We present middleware—called VAMOS (“Vigilant Algorithmic Monitoring of Software”)—which facilitates the addition of best-effort third-party monitors to deployed software. The primary goals of our middleware are (i) performance

(keeping pace at low overhead), (ii) flexibility (compatibility with a wide range of heterogeneous event sources that connect the monitor with the monitored software, and with a wide range of formal specification languages that can be compiled into VAMOS), and (iii) ease-of-use (the middleware relieves the designer of the monitor from system and code instrumentation concerns).

All of these goals are fairly standard, but VAMOS’ particular design tradeoffs center around making it as easy as possible to create a best-effort third-party monitor of actual software without investing much time into low-level details of instrumentation or load management. In practice, instrumentation—enriching the monitored system with code that is gathering observations on whose basis the monitor generates verdicts—is a key part of writing a monitoring system and affects key performance characteristics of the monitoring setup [11]. These considerations become even more important in third-party monitoring, where the limited knowledge of and access to the monitored software may force the monitor to spend more computational effort to re-derive information that it could not observe, or combine it from smaller pieces obtained from more (and different) sources. By contrast, current implementations of monitor specification languages mostly offer either very targeted instrumentation support for particular systems or some general-purpose API to receive events, or both, but little to organize multiple heterogeneous event streams, or to help with the kinds of best-effort performance considerations that we are concerned with. Thus, VAMOS fills a gap left open by existing tools.

Our vision for VAMOS is that users writing a best-effort third-party monitor start by selecting configurable instrumentation tools from a rich collection. This collection includes tools that periodically query webservices, generate events for relevant system calls, observe the interactions of web servers with clients, and of course standard code instrumentation tools. The configuration effort for each such *event source* largely consists of specifying patterns to look for and what events to generate for them. VAMOS then offers a simple specification language for filtering and altering events coming from the event sources, and simple yet expressive event recognition rules that produce a single, global event stream by combining events from a (possibly dynamically changing) number of event sources. Lastly, monitoring code as it is more generally understood—which could be written directly or generated from existing tools for run-time verification like LTL formulae [46], or stream verification specifications [8] such as TeSSLa [40]—processes these events to generate verdicts about the monitored system.

VAMOS thus represents middleware between event sources that emit events and higher-level monitoring code, abstracting away many low-level details about the interaction between the two. Users can employ both semi-synchronous and completely asynchronous [11] interactions with any or all event sources. Between these two extremes, to decouple the higher-level monitoring code’s performance from the overhead incurred by the instrumentation, while putting a bound on how far the monitoring code can lag behind the monitored system, we provide a simple load-shedding mechanism that we call *autodrop buffers*, which are buffers that drop events when the monitoring code cannot keep up with the rate of in-

coming events, while maintaining summarization data about the dropped events. This summarization data can later be used by our event recognition system when it is notified that events were dropped; some standard monitoring specification systems can handle such *holes* in their event streams automatically [41, 31, 53]. The rule-based event recognition system allows grouping and ordering buffers dynamically to prioritize or rotate within variable sets of similar event sources, and specifying patterns over multiple events and buffers, to extract and combine the necessary information for a single global event stream.

Data from event sources is transferred to the monitor using efficient lock-free buffers in shared memory inspired by Cache-Friendly Asymmetric Buffers [28]. These buffers can transfer over one million events per second per event source on a standard desktop computer. Together with autodrop buffers, this satisfies our performance goal while keeping the specification effort low. As such, VAMOS resembles a single-consumer version of an event broker [17, 57, 47, 54, 25, 1] specialized to run-time monitoring.

The core features we built VAMOS around are not novel on their own, but to the best of our knowledge, their combination and application to simplify best-effort third-party monitoring setups is. Thus, we make the following contributions:

- We built middleware to connect higher-level monitors with event sources, addressing particular challenges of best-effort third-party monitoring (Section 2), using a mixture of efficient inter-process communication and easy-to-use facilities for load management (Section 3) on one hand, and *buffer groups* and other event recognition abstractions (Section 4) on the other hand.
- We implemented a compiler for VAMOS specifications, a number of event sources, and a connector to TeSSLa [40] monitors (Section 5).
- We conducted some stress-test experiments using our framework, as well as a case study in which we implemented a monitor looking for data races, providing evidence of the feasibility of low-overhead third-party monitoring with simple specifications (Section 6).

## 2 Architectural Overview

Writing a run-time monitor can be a complex task, but a large number of tools express logical reasoning over streams of run-time observations exists [18, 23, 40, 26, 33, 15, 48]. However, trying to actually obtain a concrete stream of observations from a real system introduces a very different set of concerns, which in turn have a huge effect on the performance properties of run-time monitoring [11].

The goal of VAMOS is to simplify this critical part of setting up a monitoring system, using the model shown in Figure 1. On the left side, we assume an arbitrary number of distinct event sources directly connected to the monitor. This is particularly important in third-party monitoring, as information may need to be collected from multiple different sources instead of just a single program, but can be also useful in other monitoring scenarios, e.g. for multithreaded programs.

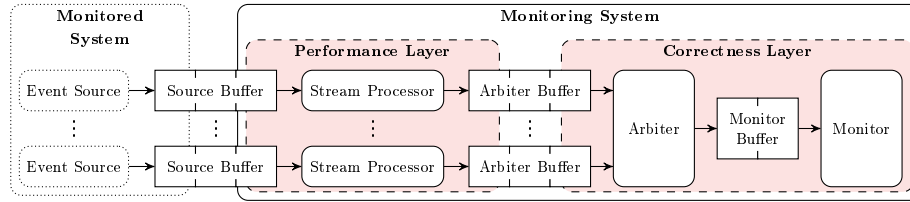


Fig. 1. The components of a VAMOS setup.

The right-most component is called the *monitor*, representing the part of the monitoring system that is typically generated by a monitoring specification tool, usually based on a single global event stream. As middleware, VAMOS connects the two, providing abstractions for common issues that monitor writers would otherwise have to address with boilerplate, but still complicated code.

Given that there are multiple event sources providing their own event streams, but only one global event stream consumed by the monitor, a key aspect is merging the incoming streams into one, which happens in the *arbiter*. Third-party monitoring often cannot rely on the source-code-based instrumentation that is otherwise common [13, 15, 4, 20, 24]; for example, TeSSLa<sup>1</sup> [40] comes with a basic way of instrumenting C programs by adding annotations into the specification that identify events with function calls or their arguments. Instead, it has to rely on things that can be reliably observed and whose meaning is clear, for example system calls, calls to certain standard library functions, or any other information one can gather from parts of the environment one controls, such as sensors or file system. These do not necessarily correspond in a straightforward way to the events one would like to feed into the higher-level monitor and thus need to be combined or split up in various ways. For example, when a program writes a line to the standard output, the data itself might be split into multiple system calls or just be part of a bigger one that contains multiple lines, and there are also multiple system calls that could be used. Therefore, the arbiter provides a way to specify a rule-based event recognition system to generate higher-level events from combinations of events on the different event sources.

Another common assumption in monitoring systems is some global notion of time that can be used to order events. This is not necessarily true for multiple, heterogeneous event sources, and even just observing the events of a multi-threaded program can cause events to arrive in an order that does not represent causality. VAMOS arbiter specifications are flexible enough to support many user-defined ways of expressing ways of merging events into a single global stream.

Doing this kind of sorting and merging and then potentially arbitrarily complex other computations in both the arbiter and the monitor may take longer than it takes the monitored system to generate events. Especially in third-party monitoring, a monitor may have to reconstruct information that is technically

<sup>1</sup> We keep referring to TeSSLa in the rest of the paper and also chose to use it in our implementation because it is one of the most easily available existing tools we could find. In general, the state of the field is that, while many papers describing similar tools exist, few are actually available [47].

```

1 stream type Observation { Op(arg : int, ret : int); }
2 event source Program : Observation to autodrop(16,4)
3 arbiter : Observation {
4     on Program: hole(n) | ;
5     on Program: Op(arg, ret) | yield;
6 }
7 monitor(2) { on Op(arg, ret) $$ CheckOp(arg, ret); $$ }

```

**Listing 1.1.** A basic asynchronous best-effort monitor.

present in the monitored system but cannot be observed, or, worse, the monitor may have to consider multiple different possibilities if information cannot be reliably recomputed. However, as part of our performance goal, we want the monitor to not lag too far behind the monitored system. Therefore, our design splits the monitoring system into the *performance* and *correctness* layers. In between the two, events may be dropped as a simple load-shedding strategy.

The performance layer, on the other hand, sees all events and processes each event stream in parallel. *Stream processors* enable filtering and altering the events that come in, reducing pressure and computational load on the correctness layer. This reflects that in third-party monitoring, observing coarse-grained event types like system calls may yield many uninteresting events. For example, all calls to read may be instrumented, but only certain arguments make them interesting.

**A Simple Example** Listing 1.1 shows a full VAMOS specification (aside from the definition of custom monitoring code in a C function called `CheckOp`). *Stream types* describe the kinds of events and the memory layout of their data that can appear in a particular buffer; in this example, streams of type `Observation` contain only one possible event named `Op` with two fields of type `int`. For source buffers—created using event source descriptions as in line 2—these need to be based on the specification of the particular event source. Each event source is associated with a *stream processor*; if none is given (as in this example), a default one simply forwards all events to the corresponding arbiter buffer, here specified as an *autodrop* buffer that can hold up to 16 events and when full keeps dropping them until there is again space for at least four new events. Using an autodrop buffer means that in addition to the events of the stream type, the arbiter may see a special *hole* event notifying it that events were dropped. In this example, the arbiter simply ignores those events and forwards all others to the monitor, which runs in parallel to the arbiter with a blocking event queue of size two, and whose behavior we implemented directly in C code between `$$` escape characters.

### 3 Efficient Instrumentation

Our goals for the performance of the monitor are to not incur too much overhead on the monitored system, and for the monitor to be reasonably up-to-date in terms of the lag between when an event is generated and when it is processed. The

key features VAMOS offers to ensure these properties while keeping specifications simple are related to the performance layer, which we discuss here.

### 3.1 Source Buffers and Stream Processors

Even when instrumenting things like system calls, in order to extract information from them in a consistent state, the monitored system will have to be briefly interrupted while the instrumentation copies the relevant data. A common solution is to write this data to a log file that the monitor is incrementally processing. This approach has several downsides. First, in the presence of multiple threads, accesses to a single file require synchronization. Second, the common use of string encodings requires extra serialization and parsing steps. Third, file-based buffers are typically at least very large or unbounded in size, so slower monitors eventually exhaust system resources. Finally, writing to the log uses relatively costly system calls. Instead, VAMOS event sources transmit raw binary data via channels implemented as limited-size lock-free ring buffer in shared memory, limiting instrumentation overhead and optimizing throughput [28]. To avoid expensive synchronization of different threads in the instrumented program (or just to logically separate events), VAMOS also allows dynamically allocating new event sources, such that each thread can write to its own buffer(s). The total number of event sources may therefore vary across the run of the monitor.

For each event source, VAMOS allocates a new thread in the performance layer to process events from this source<sup>2</sup>. In this layer, event processors can filter and alter events before they are forwarded to the correctness layer, all in a highly parallel fashion. A default event processor simply forwards all events. The computations done here should be done at the speed at which events are generated on that particular source, otherwise the source buffer will fill up and eventually force the instrumentation to wait for space in the buffer.

### 3.2 Autodrop Buffers

As we already stated, not all computations of a monitor may be able to keep up with the monitored system. Our design separates these kinds of computations into the correctness layer, which is connected with the performance layer via *arbiter buffers*. The separation is achieved by using *autodrop buffers*. These buffers provide the most straightforward form of load management via load shedding [58]: if there is not enough space in the buffer, it gathers summarization information (like the count of events since the buffer became full) and otherwise drops the events forwarded to it. Once free space becomes available in the buffer, it automatically inserts a special *hole* event containing the summarization information. The summarization ensures that not all information about dropped

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<sup>2</sup> When event sources can be dynamically added, the user may specify a limit to how many of them can exist concurrently to avoid accumulating buffers the monitor cannot process fast enough. When that limit is hit, new event sources are rejected and the instrumentation drops events that would be forwarded to them.

events is lost, which can help to reduce the impact of load shedding. At minimum, the existence of the *hole* event alone makes a difference in monitorability compared to not knowing whether any events have been lost [34], and is used as such in some monitoring systems [31, 41, 53].

In addition to autodrop buffers, arbiter buffers can also be finite-size buffers that block when space is not available, or infinite-size buffers. The former may slow down the stream processor and ultimately the event source, while the latter may accumulate data and exhaust available resources. For some event sources, this may not be a big risk, and it eliminates the need to deal with *hole* events.

## 4 Event Recognition, Ordering, and Prioritization

VAMOS’ arbiter specifications are a flexible, yet simple way to organize the information gathered from a—potentially variable—number of heterogeneous event sources. In this section, we discuss the key relevant parts of such specifications—a more complete specification can be found in Appendix A.

### 4.1 Arbiter Rules

We already saw simple arbiter rules in Listing 1.1, but arbiter rules can be much more complex, specifying arbitrary sequences of events at the front of arbitrarily many buffers, as well as buffer properties such as a minimum number of available events and emptiness. Firing a rule can also be conditioned by an arbitrary boolean expression. For example, one rule in the *Bank* example we use in our evaluation in Section 6 looks as follows:

```

1 on Out : transfer(t2, src, tgt) transferSuccess(t4) |,
2   In  : numIn(t0, act) numIn(t1, acc) numIn(t3, amnt) |
3   where $$ t2 == t0 + 4 $$
4   $$ $yield SawTransfer(src, tgt, amnt); ... $$
```

This rule matches multiple events on two different buffers (In and Out), describing a series of user input and program output events that together form a single higher-level event SawTransfer, which is forwarded to the *monitor* component of the correctness layer. Rules do not necessarily consume the events they have looked at; some events may also just serve as a kind of lookahead. The “|” character in the events sequence pattern separates the consumed events (left) from the lookahead (right). Code between \$\$ symbols can be arbitrary C code with some special constructs, such as the \$yield statement (to forward events) above.

The rule above demonstrates the basic event-recognition capabilities of arbiters. By ordering the rules in a certain way, we can also prioritize processing events from some buffers over others. Rules can also be grouped into *rule sets* that a monitor can explicitly switch between in the style of an automaton.

### 4.2 Buffer Groups

The rules shown so far only refer to arbiter buffers associated with specific, named event sources. As we mentioned before, VAMOS also supports creating

event sources dynamically during the run of the monitoring system. To be able to refer to these in arbiter rules, we use an abstraction we call *buffer groups*.

As the name suggests, buffer groups are collections of arbiter buffers whose membership can change at run time. They are the only way in which the arbiter can access dynamically created event sources, so to allow a user to distinguish between them and manage associated data, we extend stream types with *stream fields* that can be read and updated by arbiter code. Buffer groups are declared for a specific stream type, and their members have to have that stream type<sup>3</sup>. Therefore, each member offers the same stream fields, which we can use to compare buffers and order them for the purposes of iterating through the buffer group. Now the arbiter rules can also be `choice` blocks with more rules nested within them, as follows (`Both` is a buffer group and `pos` is a stream field):

```

1  choose F, S from Both {
2    on F : Prime(n,p) | where $$ $F.pos < $$S.pos $$
3    $$ ... $$
4    on F : hole(n) |
5    $$ $F.pos = $F.pos + n; $$
6  }
```

This rule is a slightly simplified version of one in the *Primes* example in Section 6. This example does not use dynamically created buffers, but only has two event sources, and uses the ordering capabilities of buffer groups to prioritize between the buffers based on which one is currently “behind” (expressed in the stream field `pos`, which the buffer group `Both` is ordered by). The `choose` rule tries to instantiate its variables with distinct members from the buffer group, trying out permutations in the lexicographic extension of the order specified for the buffer group. If no nested rule matches for a particular instantiation, the next one in order is tried, and the `choose` rule itself fails if no instantiation finds a match.

To handle dynamically created event sources, corresponding stream processor rules specify a buffer group to which to add new event sources, upon which the arbiter can access them through `choose` rules. In most cases, we expect that `choose` blocks are used to instantiate a single buffer, in which case we only need to scan the buffer group in its specified order. Here, a round-robin order allows for fairness, while field-based orderings allow more detailed control over buffers prioritization, as it may be useful to focus on a few buffers at the expense of others, as in our above example.

Another potential option for ordering schemes for buffer groups could be based on events waiting in them, or even the values of those events’ associated data. VAMOS currently does not support this because it makes sorting much more expensive—essentially, all buffers may have to be checked in order to determine the order in which to try matching them against further rules. Some of our experiments could have made use of such a feature, but in different ways—future work may add mechanisms that capture some of these ways.

<sup>3</sup> Note that stream processors may change the stream type between the source buffer and arbiter buffer, so event sources may use different types, but their arbiter buffers may be grouped together if processed accordingly.



## 5 Implementation

In this section, we briefly review the key components of our implementation.

### 5.1 Source Buffers and Event Sources

The source buffer library allows low-overhead interprocess communication between a monitored system and the monitor. It implements lock-free asynchronous ring buffers in shared memory, inspired by Cache-Friendly Asymmetric Buffering [28], but extended to handle entries larger than 64 bits<sup>4</sup>. The library allows setting up an arbitrary number of source buffers with a unique name, which a monitor can connect to explicitly, and informing such connected monitors about dynamically created buffers. A user can also provide stream type information so connecting monitors can check for binary compatibility.

We have used the above library to implement an initial library of event sources: one that is used for detecting data races, and several which use either *DynamoRIO* [9] (a dynamic instrumentation framework) or the *eBPF* subsystem of the Linux Kernel [10, 27, 49] to intercept the `read` and `write` (or any other) system calls of an arbitrary program, or to read and parse data from file descriptors. The read/write related tools allow specifying an arbitrary number of regular expressions that are matched against the traced data, and associated event constructors that refer to parts of the regular expressions from which to extract the relevant data. Example uses of these tools are included in our artifact [12].

### 5.2 The VAMOS Compiler and the TeSSLa Connector

The compiler takes a VAMOS specification described in the previous sections and turns it into a C program. It does some minimal checking, for example whether events used in parts of the program correspond to the expected stream types, but otherwise defers type-checking to the C compiler. The generated program expects a command-line argument for each specified event source, providing the name of the source buffer created by whatever actual event source is used. Event sources signal when they are finished, and the monitor stops once all event sources are finished and all events have been processed.

The default way of using TeSSLa for online monitoring is to run an offline monitor incrementally on a log file of serialized event data from a single global event source. A recent version of TeSSLa [32] allows generating Rust code for the stream processing system with an interface to provide events and drive the stream processing directly. Our compiler can generate the necessary bridging code and replace the *monitor* component in VAMOS with a TeSSLa Rust monitor. We used TeSSLa as a representative of higher-level monitoring specification tools; in principle, one could similarly use other standard monitor specification languages, thus making it easier to connect them to arbitrary event sources.

<sup>4</sup> Entries have the size of the largest event consisting of its fixed-size fields and identifiers for variable-sized data (strings) transported in separately managed memory.

## 6 Evaluation

Our stated design goals for VAMOS were (i) performance, (ii) flexibility, and (iii) ease-of-use. Of these, only the first is truly quantitative, and the majority of this section is devoted to various aspects of it. We present a number of benchmark programs, each of which used VAMOS to retrieve events from different event sources and organize them for a higher-level monitor in a different way, which provides some qualitative evidence for its flexibility. Finally, we present a case study to build a best-effort data-race monitor (Section 6.4), whose relative simplicity provides qualitative evidence for VAMOS’ ease of use.

In evaluating performance, we focus on two critical metrics:

1. How much overhead does monitoring impose on the monitored system? We measure this as the difference of wall-clock running times.
2. How well can a best-effort third-party monitor cover the behavior of the monitored program? We measure this as the portion of errors a monitor can (not) find.

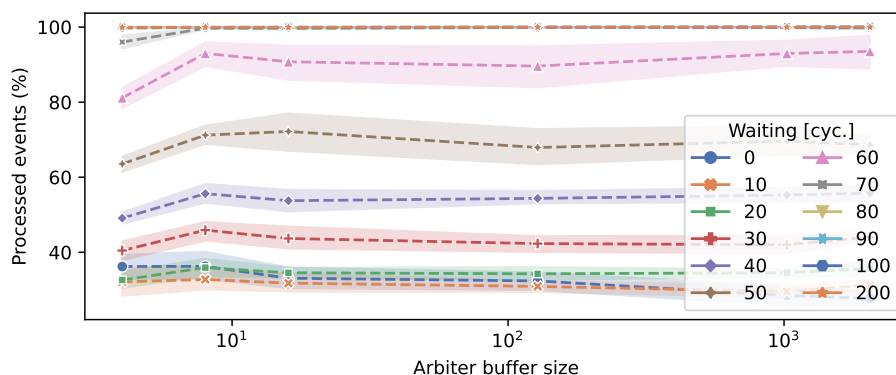
Our core claim is that VAMOS allows building useful best-effort third-party monitors for programs that generate hundreds of thousands of events per second without a significant slow down of the programs beyond the unavoidable cost of generating events themselves. We provide evidence that corroborates this claim based on three artificial benchmarks that vary various parameters and one case study implementation of a data race monitor that we test on 391 benchmarks taken from SV-COMP 2022 [7].

**Experimental setup** All experiments were run on a common personal computer with 16 GB of RAM and an *Intel(R) Core(TM) i7-8700* CPU with 6 physical cores running on 3.20 GHz frequency. Hyper-Threading was enabled and dynamic frequency scaling disabled. The operating system was Ubuntu 20.04. All provided numbers are based on at least 10 runs of the relevant experiments.

### 6.1 Scalability Tests

Our first experiment is meant to establish the basic capabilities of our arbiter implementation. An event source sends 10 million events carrying a single 64-bit number (plus 128 bits of metadata), waiting for some number of cycles between each event. The performance layer simply forwards the events to autodrop buffers of a certain size, the arbiter retrieves the events, including holes, and forwards them to the monitor, which keeps track of how many events it saw and how many were dropped. We varied the number of cycles and the arbiter buffer sizes to see how many events get dropped because the arbiter cannot process them fast enough—the results can be seen in Figure 2.

At about 70 cycles of waiting time, almost all events could be processed even with very small arbiter buffer sizes (4 and up). In our test environment, this corresponds to a delay of roughly 700 ns between events, which means that VAMOS is able to transmit approximately 1.4 million of events per second.



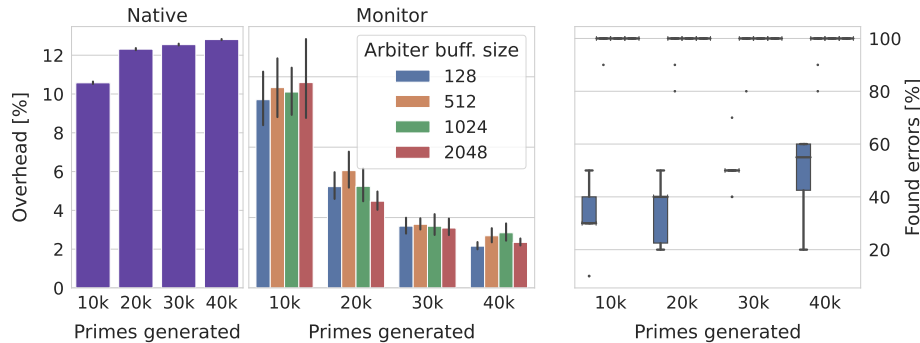
**Fig. 2.** The percentage of events that reached the final stage of the monitor in a stress test where the source sends events rapidly. Parameters are different arbiter buffer sizes (x-axis) and the delay (*Waiting*) of how many empty cycles the source waits between sending individual events. The shading around lines shows the 95% confidence interval around the mean of the measured value. The source buffer was 8 pages large, which corresponds to a bit over 1300 events.

## 6.2 Primes

As a stress-test where the monitor actually has some work to do, this benchmark compares two parallel runs of a program that generates streams of primes and prints them to the standard output, simulating a form of differential monitoring [44]. The task of the monitor is to compare their output and alert the user whenever the two programs generate different data. Each output line is of the form  $\#n : p$ , indicating that  $p$  is the  $n$ th prime. This is easy to parse using regular expressions, and our DynamoRIO-based instrumentation tool simply yields events with two 32-bit integer data fields ( $n$  and  $p$ ).

While being started at roughly the same time, the programs as event sources run independently of each other, and scheduling differences can cause them to run out of sync quickly. To account for this, a VAMOS specification needs to allocate large enough buffers to either keep enough events to make up for possible scheduling differences, or at least enough events to make it likely that there is some overlap between the parts of the two event streams that are not automatically dropped. The arbiter uses the event field for the index variable  $n$  to line up events from both streams, exploiting the buffer group ordering functionality described in Section 4.2 to preferentially look at the buffer that is “behind”, but also allowing the faster buffer to cache a limited number of events while waiting for events to show up on the other one. Once it has both results for the same index, the arbiter forwards a single pair event to the monitor to compare them.

Figure 3 shows results of running this benchmark in 16 versions, generating between 10 000 and 40 000 primes with arbiter buffer sizes ranging between 128 and 2024 events. The overheads of running the monitor are small, do not differ between different arbiter buffer sizes, and longer runs amortize the initial cost of dynamic instrumentation. We created a setting where one of the programs



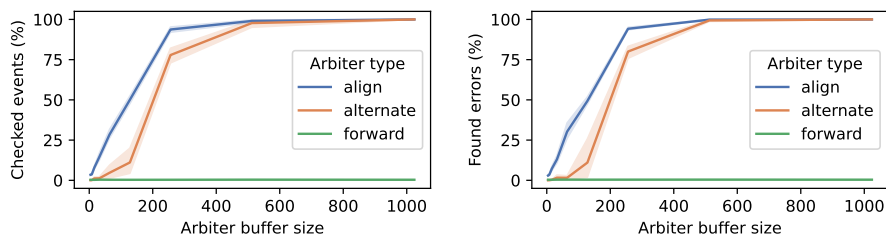
**Fig. 3.** Overheads (left) and percentage of found errors (right) in the primes benchmark for various numbers of primes and arbiter buffer sizes relative to DynamoRIO-optimized but not instrumented runs. DynamoRIO was able to optimize the program so much that the native binary runs slower than the instrumented one.

generates a faulty prime about once every 10 events and measured how many of these discrepancies the monitor can find (which depends on how many events are dropped). Unsurprisingly, larger buffer sizes are better at balancing out the scheduling differences that let the programs get out of sync. As long as the programs run at the same speed, there should be a finite arbiter buffer size that counters the desynchronization. In these experiments, this size is 512 elements.

**Primes with TeSSLa** We experimented with a variation of the benchmark that uses a very simple TeSSLa [16, 40] specification receiving two streams for each prime generator (i.e., four streams in total): one stream of indexed primes as in the original experiment, and the other with hole events. The specification expects the streams to be perfectly lined up and checks that, whenever the last-seen pairs on both streams have the same index, they also contain the same prime (and ignores non-aligned pairs of primes). We wrote three variants of an arbiter to go in front of that TeSSLa monitor:

1. the *forward* arbiter just forwards events as they come; it is equivalent to writing a script that parses output of generators and (atomically) feeds events into a pipe from which TeSSLa reads events.
2. the *alternate* arbiter always forwards the event from the stream where we have seen fewer events so far; if streams happen to be aligned (that is, contain no or only equally-sized *hole* events), the events will perfectly alternate.
3. the *align* arbiter is the one we used in our original implementation to intelligently align both streams

Figure 4 shows the impact of these different arbiter designs on how well the monitor is able to do its task, and that indeed more active arbiters yield better results—without them, the streams are perfectly aligned less than 1% of the time. While one could write similar functionality to align different, unsynchronized



**Fig. 4.** Percentage of primes checked and errors found (of 40 000 events in total) by the TeSSLa monitor for different arbiter specifications and arbiter buffer sizes.

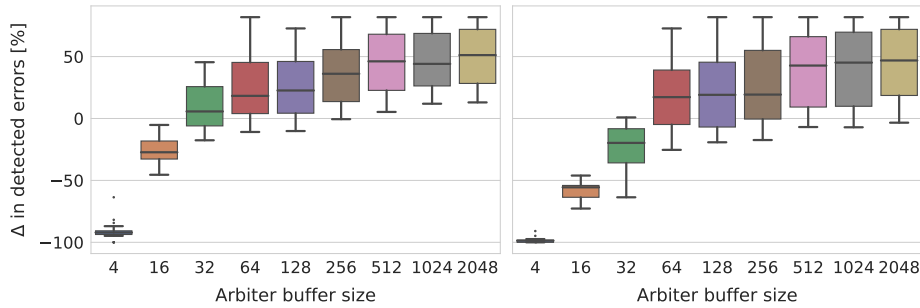
streams in TeSSLa directly, the language does not easily support this. As such, a combination of TeSSLa and VAMOS allows simpler specifications in a higher-level monitoring language, dealing with the correct ordering and preprocessing of events on the middleware level.

### 6.3 Bank

In this classic verification scenario, we wrote an interactive console application simulating a banking interface. Users can check bank account balances, and deposit, withdraw, or transfer money to and from various accounts. The condition we want to check is that no operations should be permitted that would allow an account balance to end up below 0.

We use an event source that employs DynamoRIO [9] to dynamically instrument the program to capture its inputs and outputs, which it parses to forward the relevant information to the monitor. The monitor in turn starts out with no knowledge about any of the account balances (and resets any gathered knowledge when hole events indicate that some information was lost), but discovers them through some of the observations it makes: the result of a check balance operation gives precise knowledge about an account’s balance, while the success or failure of the deposit/withdraw/transfer operations provides lower and upper bounds on the potential balances. For example, if a withdrawal of some amount fails, this amount provides an upper bound on an account’s balance, and any higher successive withdrawal attempt must surely fail too.

In the spirit of third-party monitoring, however, the stateful interface does not necessarily make it easy to derive these higher level events. For example, there is no individual confirmation that says that the withdrawal of some amount from some account was successful or not. Instead, the user selects an account, then the withdraw action, is then prompted which amount they would like to withdraw from said account, and after entering said amount, the system only displays a message that the withdrawal failed or was successful. The event source parses each individual step and provides them on two separate streams, one for the inputs and one for the outputs. This is where VAMOS’ higher-level event recognition capabilities (see also the example in Section 4.1) allow the arbiter



**Fig. 5.** Results of monitoring a simple banking simulator with VAMOS monitor (left) and TeSSLa monitor (right). Boxplots show the difference in the number of reported errors versus the number of errors the application made, in percent.

to recognize the higher-level events to forward to the monitor, which itself is therefore again much easier to specify.

To conduct measurements, we randomly generated 10 000 (well-formed) inputs and fed them to the banking application as fast as possible. We also let the application generate erroneous outputs (wrong balances, swapping success and failure messages) at random and measured how many those our best-effort third-party monitor was able to detect. The size of the source buffer was one page (128 events) and we varied the size of arbiter buffers from 4 to 2048.

The heavyweight instrumentation we used in this scenario caused the banking application to run through its script about 40% slower than without instrumentation for all sizes of the arbiter buffer, which is more than in our other benchmarks, but seems still plausible for interactive programs, and could be much more optimized. Our second metric is how many errors the monitor actually detects. Figure 5 shows this for both the monitor we described above and a TeSSLa variant that only considers exact knowledge about account balances (no upper or lower bounds) and thus finds fewer errors, demonstrating both an alternate monitor design and the use of our TeSSLa connector. The results vary quite a bit with arbiter buffer sizes and between runs, and the monitor may report more errors than were inserted into the run. This is because, first, especially with smaller buffer sizes, the autodrop buffers may drop a significant portion (up to 60% at arbiter buffer size 4, 5% at size 256) of the events, but the monitor needs to see a contiguous chunk of inputs and outputs to be able to gather enough information to find inconsistencies. Second, some errors cause multiple inconsistencies: when a transfer between accounts is misreported as successful or failed when the opposite is true, the balances (or bounds) of two accounts are wrong. Overall, both versions of the monitor were able to find errors with even smaller sizes of arbiter buffers, and increasing numbers improved the results steadily, matching the expected properties of a best-effort third-party monitor.

#### 6.4 Case Study: Data Race Detection

While our other benchmarks were written artificially, we also used VAMOS to develop a best-effort data race monitor. Most tools for dynamic data race detection use some variation of the Eraser algorithm [50]: obtain a single global sequence of synchronization operations and memory accesses, and use the former to establish happens-before relationships whenever two threads access the same memory location in a potentially conflicting way. This entails keeping track of the last accessing threads for each location, as well as of the ways in which any two threads might have synchronized since those last accesses. Implemented naïvely, every memory access causes the monitor to pause the thread and atomically update the global synchronization state. Over a decade of engineering efforts directed at tools like ThreadSanitizer [51] and Helgrind [56] have reduced the resulting overhead, but it can still be substantial.

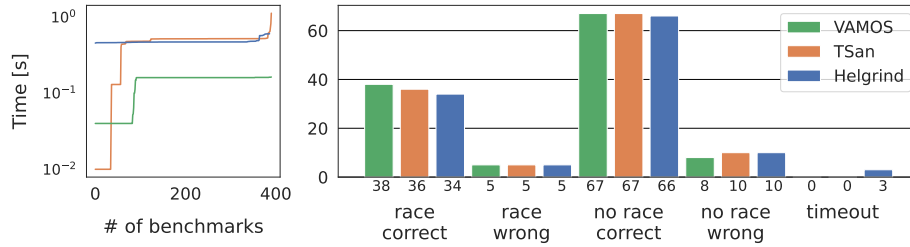
VAMOS enabled us to develop a similar monitor at significantly reduced engineering effort in a key area: efficiently communicating events to a monitor running in parallel in its own process, and building the global sequence of events. To build our monitor, we used ThreadSanitizer’s source-code-based approach<sup>5</sup> to instrument relevant code locations, and for each such location, we reduce the need for global synchronization to fetching a timestamp from an atomically increased counter. Based on our facilities for dynamically creating event sources, each thread forms its own event source to which it sends events. In the correctness layer, the arbiter builds the single global stream of events used by our implementation of a version of the Goldilocks [21] algorithm (a variant of Eraser [50]), using the timestamps to make sure events are processed in the right order. Autodrop buffers may drop some events to avoid overloading the monitor; when this happens to a thread, we only report data races that the algorithm finds if all involved events were generated after the last time that events were dropped. This means that our tool may not find some races, often those that can only be detected looking at longer traces. However, it still found many races in our experiments, and other approaches to detecting data races in best-effort ways have similar restrictions [55].

Our implementation (contained in our artifact [12]) consists of:

- a straightforward translation of the pseudocode in [21], using the C++ standard library `set` and `map` data structures, with extensions to handle holes;
- a small VAMOS specification to retrieve events from the variable number of event streams in order of their timestamps to forward to the monitor; the biggest complication here is deciding when to abandon looking for the next event in the sequence if it may have been dropped;
- an LLVM [39] instrumentation pass post-processing ThreadSanitizer’s instrumentation to produce an event source compatible with VAMOS.

As such, we were able to use VAMOS to build a reasonable best-effort data-race monitor with relatively little effort, providing evidence that our ease-of-use

<sup>5</sup> This decision was entirely to reduce our development effort; a dynamic instrumentation source could be swapped in without other changes.



**Fig. 6.** Comparing running times of the three tools on all 391 benchmarks (left) and the correctness of their verdicts on the subset of 118 benchmarks for which it was possible to determine the ground truth (right). *Race vs. no race* indicates whether the tool found at least one data race, *correct vs. wrong* indicates whether that verdict matches the ground truth. For benchmarks with unknown ground truth, the three tools agreed on the existence of data races more than 99% of the time.

design goal was achieved. To evaluate its performance, we tested it on 391 SV-COMP [7] concurrency test cases supported by our implementation, and compared it to two state-of-the-art dynamic data race detection tools, ThreadSanitizer [51] and Helgrind [56]. Figure 6 shows that the resulting monitor in most cases caused less overhead than both ThreadSanitizer and Helgrind in terms of time while producing largely the same (correct) verdicts.

## 7 Related Work

As mentioned before, VAMOS’ design features a combination of ideas from works in run-time monitoring and related fields, which we review in this section.

**Event Brokers/Event Recognition** A large number of event broker systems with facilities for event recognition [17, 57, 54, 25, 1] already exist. These typically allow arbitrary event sources to connect and submit events, and arbitrarily many observers to subscribe to various event feeds. Mansouri-Samani and Sloman [43] outlined the features of such systems, including filtering and combining events, merging multiple monitoring traces into a global one, and using a database to store (parts of) traces and additional information for the longer term. Modern industrial implementations of this concept, like Apache Flink [1], are built for massively parallel stream processing in distributed systems, supporting arbitrary applications but providing no special abstractions for monitoring, in contrast to more run-time-monitoring-focused implementations like ReMinds [57]. Complex event recognition systems also sometimes provide capabilities for load-shedding [58], of which autodrop buffers are the simplest version. Most event recognition systems provide more features than VAMOS, but are also harder to set up for monitoring; in contrast, VAMOS offers a simple specification language that is efficient and still flexible enough for many monitoring scenarios.



**Stream Run-Time Verification** LoLa [18, 23], TeSSLa [40], and Striver [26] are stream run-time verification [8] systems that allow expressing a monitor as a series of mutually recursive data streams that compute their current values based on each other’s values. This requires some global notion of time, as the streams are updated with new values at time ticks and refer to values in other streams relative to the current tick, which is not necessarily available in a heterogeneous setting. Stream run-time verification systems also do not commonly support handling variable numbers of event sources. Some systems allow for dynamically instantiating sub-monitors for parts of the event stream [23, 3, 48, 6] in a technique called *parametric trace slicing* [14]. This is used for logically splitting the events on a given stream into separate streams, making them easier to reason about, and can sometimes be exploited for parallelizing the monitor’s work. These additional streams are internal to the monitoring logic, in contrast, VAMOS’ ability to dynamically add new event sources affects the monitoring system’s outside connections, while, internally, the arbiter still unifies the events coming in on all such connections into one global stream.

**Instrumentation** The two key questions in instrumentation revolve around the technical side of how a monitor accesses a monitored system as well as the behavioral side of what effects these accesses can have. On the technical side, static instrumentation can be either applied to source code [35, 33, 38, 36, 29, 39] or compiled binaries [22, 19], while dynamic instrumentation, like DyanmoRIO, is applied to running programs [9, 45, 42]. Alternatively, monitored systems or the platforms they run on may have specific interfaces for monitors already, such as PTrace and DTrace [10, 27, 49] in the Linux kernel. Any of these can be used to create an instrumentation tool for VAMOS.

On the behavioral side, Cassar et al. surveyed various forms of instrumentation between completely synchronous and offline [11]. Many systems surveyed [13, 15, 4, 20] use a form of static instrumentation that can either do the necessary monitoring work while interrupting the program’s current thread whenever an event is generated, or offer the alternative of using the interruption to export the necessary data to a log to be processed asynchronously or offline. A mixed form called *Asynchronous Monitoring with Checkpoints* allows stopping the monitored system at certain points to let the monitor catch up [24]. Our autodrop buffers instead trade precision for avoiding this kind of overhead. Aside from the survey, some systems [40] incrementalize their default offline behavior to provide a monitor that may eventually significantly lag behind the monitored system.

Executing monitoring code or even just writing event data to a file or sending it over the network is costly in terms of overhead, even more so if multiple threads need to synchronize on the relevant code. Ha et al. proposed Cache-Friendly Asymmetric Buffering [28] to run low-overhead run-time analyses on multicore platforms. They only transfer 64-bit values, which suffices for some analyses, but not for general-purpose event data. Our adapted implementation thus has to do some extra work, but shares the idea of using a lock-free single-producer-single-consumer ring buffer for low overhead and high throughput.

While we try to minimize it, we accept some overhead for instrumentation as given. Especially in real-time systems, some run-time monitoring solutions adjust the activation status of parts of the instrumentation according to some metrics of overhead, inserting hole events for phases when instrumentation is deactivated [2, 30, 5]. In contrast, the focus of load-shedding through autodrop buffers is on ensuring that the higher-level part of the monitor is working with reasonably up-to-date events while not forcing the monitored system to wait. For monitors that do not rely on extensive summarization of dropped events, the two approaches could easily be combined.

**Monitorability and Missing Events** Monitorability [37, 46] studies the ability of a runtime monitor to produce reliable verdicts about the monitored system. The possibility of missing arbitrary events on an event stream without knowing about it significantly reduces the number of monitorable properties [34]. The *autodrop* buffers of VAMOS instead insert *hole* information, which some LTL [31], TeSSLa [41], and Mealy machine [53] specifications can be patched to handle automatically. Run-time verification with state estimation [52] uses a Hidden Markov Model to estimate the data lost in missing events.

## 8 Conclusion

We have presented VAMOS, which we designed as middleware for best-effort third-party run-time monitoring. Its goal is to significantly simplify the instrumentation part of monitoring, broadly construed as the gathering of high-level observations that serve as the basis for traditional monitoring specifications, particularly for best-effort third-party run-time monitoring, which may often need some significant preprocessing of the gathered information, potentially collected from multiple heterogeneous sources. We have presented preliminary evidence that the way we built VAMOS can handle large numbers of events and lets us specify a variety of monitors with relative ease. In future work, we plan to apply VAMOS' to more diverse application scenarios, such as multithreaded web servers processing many requests in parallel, or embedded software, and to integrate our tools with other higher-level languages. If a system's behavior conforms to the expectation of a third party, this is generally recognized as inspiring a higher level of trust than if that monitor was written by the system's developers. We hope that our design can help making best-effort third-party run-time monitoring more common.

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Event	$E$	Stream Type	$T$	Event Source	$S$
Buffer Group	$B$	Stream Processor	$P$	Variable/Field	$X$
		Hole Operation	$O$		

**Fig. 7.** Identifiers

Value	$\nu \subset n \mid \text{true} \mid \text{false} \mid \dots$
Expression	$\xi \subset X \mid \nu \mid \xi + \xi \mid \dots$
Statement	$\sigma \subset \{\bar{\sigma}\} \mid X = \xi \mid \xi \mid \tau X = \xi \mid \text{if}(\xi) \sigma \text{ else } \sigma \mid \dots$
Type	$\tau \subset \text{int} \mid \text{bool} \mid \dots$
Exp Evaluation Context	$\mathbb{E} \subseteq \cdot \mid \nu + \mathbb{E} \mid \mathbb{E} + \xi \mid \dots$
Stmt Evaluation Context	$\mathbb{S} \subseteq \cdot \mid \{\mathbb{S} ++ \bar{\sigma}\} \mid X = \mathbb{E} \mid \tau X = \mathbb{E} \mid \text{if}(\mathbb{E}) \sigma \text{ else } \sigma \dots$
Base State	$\Pi$ <b>opaque</b>

**Fig. 8.** Assumed Base Language Structure

## A Language Formalization

This section formalizes the core of VAMOS in terms of an extension of some base language. Our rough notational convention is that greek letters refer to either the base language or structures of the operational semantics. Patched versions of base language constructions are marked with  $\bar{\cdot}$ . Figure 7 shows different kinds of identifiers we will use - for simplicity, we assume that all sets of identifiers are distinct and no shadowing occurs. Grammatical non-terminals that define an identifier use  $\hat{\cdot}$ , e.g.  $\hat{S}$  is the non-terminal for an event source definition. Evaluation contexts use blackboard bold, e.g.  $\mathbb{E}$ , and certain reducible non-terminals that may represent one or more other non-terminals plus a special value  $\circ$  use the marker  $\dot{\cdot}$ , as in  $\dot{a}$ . We use  $\bar{\cdot}$  to mark lists; the operator  $++$  works both as cons, snoc, and concatenate operation, and  $\square$  marks an empty list, which in some cases may also be simply elided.

Our actual language provides additional syntactic shorthands, as well as the option to limit how many dynamically created event sources can be accepted to run at the same time, and a notion of *rule sets* for the arbiter that can be switched between with a special statement; this serves to be easily able to activate and deactivate some rules in one fell swoop (see also the examples in our artifact [12]).

### A.1 Base Language

We assume some base language with standard imperative features (in our implementation: C), i.e. standard notions of values  $\nu$ , Expressions  $\xi$ , Statements  $\sigma$ , and so on, as in Figure 8.

We extend this language with a few extra forms in some places, shown in Figure 9. In particular, we add an expression for referring to source fields  $\$S.X$ , and several kinds of statements: the first can assign a new value to a source field, `drop` drops a number of events from a given buffer between the arbiter and the

Expression	$\tilde{\xi} \text{ extends } \xi ::= \dots \mid \$S.X$
Statement	$\tilde{\sigma} \text{ extends } \sigma ::= \dots \mid \$S.X = \tilde{\xi}$
	$\mid \text{ drop } n \text{ from } S \mid \text{ yield } E(\tilde{\xi})$ $\mid \text{ add } S \text{ to } B \mid \text{ remove } S \text{ from } B$
Stmt Evaluation Context	$\tilde{\mathbb{S}} \text{ extends } \mathbb{S} ::= \dots \mid \text{ yield } E(\bar{v} \uparrow \tilde{\mathbb{E}} \uparrow \tilde{\xi})$

**Fig. 9.** Extended Language

Event Definition	$\hat{E} ::= E(\Gamma) \mid E(\Gamma) \text{ creates } T$
Stream Type	$\hat{T} ::= \text{stream type } T(\Gamma) \{ \hat{E} \}$
SP Expression	$e ::= \text{forward } E(\tilde{\xi}) \mid \text{drop} \mid \text{if } \xi \text{ then } e \text{ else } e$
SP Rule	$p ::= \text{on } E(\bar{X}) e$ $\mid \text{on } E(\bar{X}) \text{ process using } P(\tilde{\xi})$ $\text{to } k \text{ include in } \bar{B} e$
Stream Processor	$\hat{P} ::= \text{stream processor } P(\Gamma) : T(\tilde{\xi}) \rightarrow T(\tilde{\xi}) \{ \bar{p} ; \hat{H} \}$
Hole Definition	$\hat{H} ::= \text{hole } E \{ \bar{X} \}$
H. Field Definition	$\hat{X} ::= X : \tau = O(E.\bar{X})$
Buffer Kind	$k ::= \text{autodrop}(n, n) \mid \text{infinite} \mid \text{blocking}(n)$
Event Source	$\hat{S} ::= \text{event source } S(\Gamma) : T \text{ process using } P(\tilde{\xi}) \text{ to } k$
Type Env	$\Gamma ::= \bar{X} : \tau$

**Fig. 10.** Performance Layer

performance layer, `yield` forwards and event from the arbiter to a monitor, and `add/remove` manage membership in buffer groups. Of these, only `yield` makes a difference to evaluation contexts as we compute the data associated with an event that is going to be forwarded to the monitor.

## A.2 The Performance Layer

Figure 10 shows the parts of the performance layer. Stream types define a number of stream fields and a list of events, each of which has a number of fields with name  $X$  and type  $\tau$ , and some of which may signal the creation of an additional event source of some stream type.

A stream processor  $\hat{P}$  may specify some arguments  $\Gamma$  to use in instantiating the fields of the stream types it uses to describe the events it assumes to receive and produce. This is mostly important for the output stream, as there is currently no way for the stream processor itself to access those fields. The stream processor then provides a list of rule definitions  $p$ , which in turn provide a pattern to match any given event against, and a stream processor expression  $e$  that is to be evaluated on a match. These expressions are limited to nested branches on base expressions  $\xi$  that eventually make a decision whether to drop the current event or forward something to arbiter. A variant of these rules is for those events that signal the existence of a new stream source, in which case the rule contains





or a variable bound in a `choose` expression. The buffer corresponding to that source can then be constrained to be either `done`, that is, there are no events on that buffer and the event source has indicated no new events will arrive, or `nothing`, which just means there are currently no events in the buffer, or some number  $n$ , which means that there need to be at least  $n$  events waiting on the buffer, or finally, a pattern match of potentially multiple events that need to match the first few events currently in the buffer. In addition to the buffer match expressions, a `where` condition  $\tilde{\xi}$  further restricts when a rule can match. If all conditions are satisfied, the extended statements in  $\tilde{\sigma}$  are executed.

In contrast to the arbiter, the monitor definition  $\hat{M}$  consists of just an indication of the size of its event queue  $n$  and a list of pattern-matching rules  $\hat{m}$  that can run arbitrary code, but does not have access to our extended expressions or statements.

#### A.4 Operational Semantics

The (small-step) operational semantics of VAMOS is non-deterministic to reflect the asynchronous nature of the various parts of the program. In particular, the arbiter, the monitor, and every stream processor work on their own threads. Some additional non-determinism in the order in which buffer match expressions are evaluated reflects an openness to certain optimizations, as discussed below. The semantics is initialized with every event that eventually shows up on each event source and as such models the possible behaviors of the system up to some event in the case of an infinite event stream.

All of this is reflected in the data structures we use to track the program state—shown in Figure 12—and the top-level semantics shown in Figure 13. The state of the program is split to make it easy to access sub-parts of it in many cases. The signature of the core stepping relation in Figure 13 is

$$\Sigma \mid \Omega \mid \Pi \vdash \Theta \rightarrow \Theta \dashv \Sigma \mid \Omega \mid \Pi$$

modelling a transition of the core state  $\Theta$  and the auxiliary state with its three components:  $\Sigma$  is a list of events representing the event queue between the arbiter and the monitor,  $\Omega$  mostly contains the code of the program as written, except that it also keeps track of membership in buffer groups (otherwise, it is just used for reference and the proof of type safety), and  $\Pi$  is the (opaque) store data structure of the base language—we do not manipulate this on our own, but thread it through for use by the base transition relation, whose signature we assume to be:

$$\Pi \vdash \sigma \dashv \Pi$$

The core VAMOS state data structure as shown in Figure 12 essentially keeps track of the threads of the monitoring system—some number of threads for the stream processors, and one each for the arbiter and the monitor. The state of the monitor is simple, as it is just the current (block of) statement(s) that is being executed—we assume that the base step relation uses the following rule

Program	$\Omega ::= \langle \bar{T}, \bar{S}, \bar{P}, \bar{B}, \hat{A}, \hat{M} \rangle$
Event	$\epsilon ::= E(\bar{v} ++ \Sigma)$
Maybe Event	$\dot{\epsilon} ::= \circ \mid \epsilon$
Event List	$\Sigma ::= \bar{\epsilon}$
Choice State	$\psi ::= \langle S, \bar{S}, \bar{S} \rangle$
Choice Stack	$\phi ::= \circ \mid \langle \bar{\psi}, \bar{a}, \bar{a} \rangle; \phi$
Processor Redex	$\dot{e} ::= \circ \mid e$
Processor State	$\rho ::= \langle S, P, \Delta, \Sigma, \dot{e}, k, \dot{\epsilon}, \Sigma \rangle \mid \langle S, P(\bar{\xi}), \Sigma, k, \overline{X \mapsto \xi}, \bar{B} \rangle$
Arbiter Redex	$\dot{a} ::= \circ \mid \hat{a} \mid \bar{\sigma}$
Arbiter Local State	$\varsigma ::= \langle \bar{B}, \bar{\rho}, \bar{B} \rangle$
Arbiter State	$\alpha ::= \langle \hat{a}, \bar{a}, \phi, \varsigma \rangle$
Monitor State	$\mu ::= \sigma$
VAMOS State	$\Theta ::= \langle \bar{\rho}, \alpha, \mu \rangle$
Processor Eval Ctx	$\mathbb{P} ::= \cdot \mid \text{forward } E(\bar{v} ++ \mathbb{E} ++ \bar{\xi}) \mid \text{if } \mathbb{E} \text{ then } e \text{ else } e$
Source State E-Ctx	$\mathbb{B} ::= \cdot \mid \langle S, P, \Delta, \Sigma, \mathbb{P}, k, \dot{\epsilon}, \Sigma \rangle \mid \langle S, P(\bar{v} ++ \mathbb{E} ++ \bar{\xi}), \Sigma, k, [], \bar{B} \rangle$ $\mid \langle S, P(\bar{v}), \Sigma, k, \overline{X \mapsto \bar{v}} ++ X \mapsto \mathbb{E} ++ \overline{X \mapsto \xi}, \bar{B} \rangle$
Arbiter Eval Ctx	$\mathbb{A} ::= \cdot \mid \langle \bar{S}, \bar{a}, \phi, \varsigma \rangle \mid \langle \text{on } \bar{b} \text{ where } \tilde{\mathbb{E}} \{ \bar{\sigma} \}, \bar{a}, \phi, \varsigma \rangle$
Monitor Eval Ctx	$\mathbb{M} ::= \bar{S}$
Eval Context	$\mathbb{X} ::= \langle \bar{\rho} ++ \mathbb{B} ++ \bar{\rho}, \alpha, \mu \rangle \mid \langle \bar{\rho}, \mathbb{A}, \mu \rangle \mid \langle \bar{\rho}, \alpha, \mathbb{M} \rangle$

**Fig. 12.** Operational Semantics Grammar

to step in blocks of code:

$$\overline{\Pi \vdash \{\{\} ++ \bar{\sigma}\} \rightarrow \{\bar{\sigma}\} \dashv \Pi}$$

In turn, we also assume that statements that have finished their work turn themselves into  $\{\}$  in the stepping relation. This means that the initial state of the monitor and the state it ends up in after executing a rule can be  $\{\}$ .

The state of a stream processor is more complex, as it keeps track of the source  $S$  it belongs to, the name  $P$  of the processor template it was instantiated from, a map  $\Delta$  of stream fields, a list  $\Sigma$  of events that may eventually show up on the event source (as discussed above), a redex  $\dot{e}$  describing the state of executing the stream processor's code ( $\circ$  means it is ready to process another event), the buffer kind  $k$  of the buffer the stream processor forwards events to, the next hole event  $\dot{\epsilon}$  under construction, and another list  $\Sigma'$  of events in said buffer. The second version of a stream processor state keeps track of stream processors as they are initializing when a stream is opened dynamically.

Finally, the arbiter state keeps track of a redex  $\dot{a}$  as well, but combines this with continuations  $\bar{a}$  for the next rules to be tried if a match fails or a stack  $\phi$  of backtracking options for choosing different kinds of event sources. The stack  $\phi$  of backtracking options in turn consists of entries which keep track of an individual permutation state  $\psi$  for each variable of a `choose` expression, the list of arbiter rules  $\bar{a}$  to be tried within that `choose` expression, and the continuation  $\bar{a}'$  of sibling rules of the `choose` expression we move on to if all potential choices are

$$\boxed{\Sigma \mid \Omega \mid \Pi \vdash \Theta \rightarrow \Theta \dashv \Sigma \mid \Omega \mid \Pi}$$

$$\frac{\Pi \mid \square \vdash \tilde{\xi} \rightarrow \tilde{\xi}' \dashv \Pi}{\Sigma \mid \Omega \mid \Pi \vdash \mathbb{X}[\tilde{\xi}] \rightarrow \mathbb{X}[\tilde{\xi}'] \dashv \Sigma \mid \Omega \mid \Pi} \quad \frac{\Sigma \mid \Omega \mid \Pi \mid \square \vdash \tilde{\sigma} \rightarrow \tilde{\sigma}' \dashv \Sigma' \mid \Omega' \mid \Pi' \mid \square}{\Sigma \mid \Omega \mid \Pi \vdash \mathbb{X}[\tilde{\sigma}] \rightarrow \mathbb{X}[\tilde{\sigma}'] \dashv \Sigma' \mid \Omega' \mid \Pi'}$$

$$\frac{\Omega \mid \Pi \vdash \rho \rightarrow \bar{\rho}' \dashv \Omega'}{\Sigma \mid \Omega \mid \Pi \vdash \mathbb{X}[\rho] \rightarrow \mathbb{X}[\bar{\rho}'] \dashv \Sigma \mid \Omega' \mid \Pi} \quad \frac{\Sigma \mid \Omega \mid \Pi \mid \bar{\rho} \vdash \alpha \rightarrow \alpha' \dashv \Sigma' \mid \Omega' \mid \Pi' \mid \bar{\rho}'}{\Sigma \mid \Omega \mid \Pi \vdash \langle \bar{\rho}, \alpha, \mu \rangle \rightarrow \langle \bar{\rho}', \alpha', \mu \rangle \dashv \Sigma' \mid \Omega' \mid \Pi'}$$

$$\frac{\Omega = \langle \bar{T}, \bar{S}, \bar{P}, \bar{B}, \text{arbiter} : T \{ \bar{a} \}, \hat{M} \rangle}{E(\bar{v}) \dashv \Sigma \mid \Omega \mid \Pi \vdash \langle \bar{\rho}, \alpha, \{ \} \rangle \rightarrow \langle \bar{\rho}, \alpha, \sigma' \rangle \dashv \Sigma \mid \Omega \mid \Pi}$$

on  $E(\bar{X}) \{ \sigma \} \in \hat{M} \quad \Omega \vdash \sigma[E(\bar{X} \mapsto \nu) : T] \rightarrow \sigma'$

Fig. 13. Operational Semantics—Top Level

exhausted without finding a match. The individual permutation state  $\psi$  consists of the name of the corresponding variable, a list  $\bar{S}$  of sources in the buffer group we have already tried in this permutation attempt, and a list  $\bar{S}'$  of sources in the buffer group that are still to be tried. Each round of checking arbiter rules keeps track of some local state  $\varsigma$ , consisting of three elements. For implementing the round-robin order of buffer groups, we keep track of the list of buffer group ids  $\bar{B}$  we have explored in this round. Furthermore, in order to stay consistent about the events in a particular buffer and the buffers in a particular buffer group, the first check that references either of them makes a local copy for the particular round, to be used later. This avoids first checking a rule that asks for an event in a particular buffer while that buffer is empty but matching on a rule that asks for two events on that buffer later when more events have concurrently appeared, and similar for checks if a buffer has a member that matches certain conditions.

The top-level rules of the stepping relation then start with general-purpose rules to evaluate expressions and statements. The empty list as the last part of the context on both sides is an argument that in more specialized cases contains the list of stream processor threads  $\bar{\rho}$ . This only applies in the arbiter, however, so in general, expressions and statements have to make progress without access to that information. In addition, the expression rule here requires that the expressions do not affect the global state of the program (this is only allowed for expressions contained in statements).

The next two rules refer to the individual stepping relations for stream processors and the arbiter, respectively. The final rule starts each cycle of the monitor processing an event by retrieving the first waiting event from the queue between the arbiter and the monitor (if any), generating appropriate local variable assignments for the pattern match variables, and prepending them to the code associated with the matched rule. All other work by the monitor is covered by the first two rules.

$$\boxed{\Omega \mid \Pi \vdash \rho \rightarrow \bar{\rho} \dashv \Omega}$$

$$\frac{\Omega = \langle \bar{T}, \bar{S}, \bar{P}, \bar{B}, \hat{A}, \hat{M} \rangle \quad \text{stream processor } P(\dots) : \dots \{ \bar{p} \dashv p' \dashv \bar{p}''; \hat{H} \} \in \bar{P} \quad p' \vdash \epsilon \rightarrow e \dashv \bar{p}}{\Omega \mid \Pi \vdash \langle S, P, \Delta, \epsilon \dashv \Sigma, o, k, n, \Sigma' \rangle \rightarrow \langle S, P, \Delta, \Sigma, e, k, n, \Sigma' \rangle \dashv \bar{p} \dashv \Omega}$$

$$\frac{\Omega = \langle \bar{T}, \bar{S}, \bar{P}, \bar{B}, \hat{A}, \hat{M} \rangle \quad k \mid \hat{H} \mid \bar{E} \mid \dot{\epsilon} \mid \Sigma' \vdash e \rightarrow e' \dashv \dot{\epsilon}' \mid \Sigma'' \quad \text{stream processor } P(\bar{X} : \tau) : T(\bar{\xi}) \rightarrow T'(\bar{\xi}') \{ \bar{p}; \hat{H} \} \in \bar{P} \quad \text{stream type } T'(\bar{X}' : \tau') \{ \bar{E} \} \in \bar{T}}{\Omega \mid \Pi \vdash \langle S, P, \Delta, \Sigma, e, k, \dot{\epsilon}, \Sigma' \rangle \rightarrow [\langle S, P, \Delta, \Sigma, e', k, \dot{\epsilon}', \Sigma'' \rangle] \dashv \Omega}$$

$$\frac{k = \text{autodrop}(n', n'') \quad |\Sigma'| < n' - n''}{\Omega \mid \Pi \vdash \langle S, P, \Delta, \Sigma, e, k, \epsilon, \Sigma' \rangle \rightarrow [\langle S, P, \Delta, \Sigma, e, k, o, \Sigma' \dashv \epsilon \rangle] \dashv \Omega}$$

$$\frac{\Omega = \langle \bar{T}, \bar{S}, \bar{P}, \bar{B}, \hat{A}, \hat{M} \rangle \quad \text{stream processor } P(\bar{X} : \tau) : T(\bar{\xi}) \rightarrow T'(\bar{\xi}') \{ \bar{p}; \hat{H} \} \in \bar{P} \quad \text{stream type } T'(\bar{X}' : \tau') \{ \bar{E} \} \in \bar{T}}{\Omega \mid \Pi \vdash \langle S, P(\bar{\nu}), \Sigma, k, [], \bar{B} \rangle \rightarrow [\langle S, P(\bar{\nu}), \Sigma, k, X' \mapsto \xi'[\bar{X} \mapsto \bar{\nu}], \bar{B} \rangle] \dashv \Omega}$$

$$\frac{\Omega \vdash \bar{B} + S \dashv \Omega'}{\Omega \mid \Pi \vdash \langle S, P(\bar{\nu}'), \Sigma, k, \bar{X} \mapsto \bar{\nu}, \bar{B} \rangle \rightarrow [\langle S, P, \bar{X} \mapsto \bar{\nu}, \Sigma, o, k, o, [] \rangle] \dashv \Omega'}$$

$$\boxed{p \vdash \epsilon \rightarrow e \dashv \bar{p}} \quad \frac{}{\text{on } E(\bar{X}) \ e \vdash E(\bar{\nu}) \rightarrow e[\bar{X} \mapsto \bar{\nu}] \dashv []}$$

$$\frac{p = \text{on } E(\bar{X}) \text{ process using } P(\bar{\xi}) \text{ to } k \text{ include in } \bar{B} \ e}{p \vdash E(\bar{\nu} \dashv \Sigma) \rightarrow e[\bar{X} \mapsto \bar{\nu}] \dashv [\langle \text{fresh}_S, P(\bar{\xi}[\bar{X} \mapsto \bar{\nu}]), \Sigma, k, [], \bar{B} \rangle]}$$

$$\boxed{\Omega \vdash \bar{B} + S \dashv \Omega} \quad \frac{}{\Omega \vdash [] + S \dashv \Omega}$$

$$\frac{\Omega = \langle \bar{T}, \bar{S}, \bar{P}, \bar{B}_l \dashv \hat{B} \dashv \bar{B}_r, \hat{A}, \hat{M} \rangle \quad \Omega' = \langle \bar{P}, \bar{B}_l \dashv \hat{B}' \dashv \bar{B}_r, \hat{A}, \hat{M} \rangle \quad \hat{B} = \text{buffer group } B : T \text{ order by } o \{ \bar{S}' \} \quad \bar{S}'' = \bar{S}' \cup S \quad \hat{B}' = \text{buffer group } B : T \text{ order by } o \{ \bar{S}'' \} \quad \Omega' \vdash \bar{B}' + S \dashv \Omega''}{\Omega \vdash \bar{B}' \dashv B + S \dashv \Omega''}$$

Fig. 14. Operational Semantics—Performance Layer (High-Level)

### A.5 Performance Layer Semantics

The specific stepping rules for the performance layer are shown in Figures 14 and 15. The first rule resets the current redex to process the next event when the last one has been processed, potentially also initializing the creation of a new

event source and stream processor using the first helper relation. The second rule takes a step in evaluating the current redex using the last two helper relations below. The third rule applies only to event sources that use `autodrop` buffers—in that case, if a hole event that collects summarization information has been generated and there is enough space again to enqueue new events in the arbiter buffer, we emit that hole event and reset its slot. This also leaves space for at least one regular event, so at no point there are two consecutive `hole` events. The fourth rule takes a step in initializing a dynamically added event source - once the arguments for the stream processor are evaluated, we can start evaluating the arguments to the stream type that contains the relevant stream fields. The fifth rule finishes this initialization process once all arguments to the stream type have been evaluated. In that step, the second helper relation takes care of adding the event source to the specified list of buffer groups.

The first four rules in Figure 15 deal with the stream processor redexes; the only interesting case is when an event is forwarded, in which case the first helper relation interprets the buffer kind  $k$  to determine whether and how we can proceed. The important cases here concern the `autodrop` buffer: hole event construction (using the final two helper relations) only starts when the arbiter buffer is full, and only continues while not enough space is free. Conversely, events are only forwarded when no hole event is being constructed (if one is, the rule that emits and resets it is in Figure 14 above).

## A.6 Arbiter Semantics

The rules for the arbiter in Figure 16 start by allowing statements to step with the additional information about the stream processors, in particular, this is where the stream fields are stored. The second rule refers to the more specific rules for running the matching process below, while the last rule resets the arbiter state to look for the next rule to apply. This latter rule also checks if there are any events left to process anyway before proceeding, and also adjusts the order of event sources in each buffer group according to its specification.

The first rule of the rule matching process considers the state when a rule has matched and its statements have been executed to completion. In that case, we also clear both kinds of continuations: the rules that we might have tried next if the current one would not have matched, and the stack of potential other permutations we would try if none of those latter rules apply, either. The second rule handles that latter case, retrieving a new list of rules to try and an updated stack, while the third rule handles the former case of simply retrieving the next rule.

The next two rules are very similar and only differ in the way they apply the ordering of the buffer group, setting up the evaluation of a choice expression by pushing the initial setup for the given permutation to the stack, also storing the current arbiter rules continuation there.

The final four rules in Figure 16 deal with the top-level aspects of matching a concrete rule. This is the one case of non-determinism in this semantics that is not due to multithreading, but rather for optimization: while the arbiter needs to

$$\begin{array}{c}
 \boxed{k \mid \hat{H} \mid \overline{\hat{E}} \mid \dot{\epsilon} \mid \Sigma \vdash e \rightarrow e \dashv \dot{\epsilon} \mid \Sigma} \\
 \hline
 k \mid \hat{H} \mid \overline{\hat{E}} \mid \dot{\epsilon} \mid \Sigma \vdash \text{drop} \rightarrow \circ \dashv \dot{\epsilon} \mid \Sigma \qquad \frac{k \mid \hat{H} \mid \overline{\hat{E}} \mid \dot{\epsilon} \vdash \Sigma + E(\bar{\nu}) \rightarrow \Sigma' \dashv \dot{\epsilon}'}{k \mid \dot{\epsilon} \mid \Sigma \vdash \text{forward } E(\bar{\nu}) \rightarrow \circ \dashv \dot{\epsilon}' \mid \Sigma'} \\
 \\
 \hline
 k \mid \hat{H} \mid \overline{\hat{E}} \mid \dot{\epsilon} \mid \Sigma \vdash \text{if true then } e_1 \text{ else } e_2 \rightarrow e_1 \dashv \dot{\epsilon} \mid \Sigma \\
 \\
 \hline
 k \mid \hat{H} \mid \overline{\hat{E}} \mid \dot{\epsilon} \mid \Sigma \vdash \text{if false then } e_1 \text{ else } e_2 \rightarrow e_2 \dashv \dot{\epsilon} \mid \Sigma \\
 \\
 \boxed{k \mid \hat{H} \mid \overline{\hat{E}} \mid \dot{\epsilon} \vdash \Sigma + \epsilon \rightarrow \Sigma \dashv \dot{\epsilon}} \\
 \hline
 \text{infinite} \mid \hat{H} \mid \overline{\hat{E}} \mid \circ \vdash \Sigma + \epsilon \rightarrow \Sigma ++ \epsilon \dashv \circ \qquad \frac{k = \text{blocking}(n) \quad |\Sigma| < n}{k \mid \hat{H} \mid \overline{\hat{E}} \mid \circ \vdash \Sigma + \epsilon \rightarrow \Sigma ++ \epsilon \dashv \circ} \\
 \frac{|\Sigma| \geq n \vee (\dot{\epsilon}' \neq \circ \wedge |\Sigma| \geq n - n')}{\hat{H} \mid \overline{\hat{E}} \vdash \dot{\epsilon} + \epsilon = \dot{\epsilon}'} \qquad \frac{k = \text{autodrop}(n, n') \quad |\Sigma| < n}{k \mid \hat{H} \mid \overline{\hat{E}} \mid \circ \vdash \Sigma + \epsilon \rightarrow \Sigma ++ \epsilon \dashv \circ} \\
 \text{autodrop}(n, n') \mid \hat{H} \mid \overline{\hat{E}} \mid \dot{\epsilon} \vdash \Sigma + \epsilon \rightarrow \Sigma \dashv \dot{\epsilon}' \\
 \\
 \boxed{\hat{H} \mid \overline{\hat{E}} \vdash \dot{\epsilon} + \epsilon = \dot{\epsilon}} \\
 \frac{\overline{\hat{E}} \vdash E' : \overline{X} : \tau \quad \overline{\hat{X}} \vdash \nu + E'(\overline{X} = \nu') = \nu''}{\text{hole } E \{ \overline{\hat{X}} \} \mid \overline{\hat{E}} \vdash E(\bar{\nu}) + E'(\bar{\nu}') = E(\bar{\nu}'')} \\
 \frac{\overline{\hat{E}} \vdash E' : \overline{X} : \tau \quad \overline{\hat{X}} \vdash \nu \quad \overline{\hat{X}} \vdash \nu + E'(\overline{X} = \nu') = \nu''}{\text{hole } E \{ \overline{\hat{X}} \} \mid \overline{\hat{E}} \vdash \circ + E'(\bar{\nu}') = E(\bar{\nu}'')} \\
 \\
 \boxed{\hat{X} \vdash \nu + E(\overline{X} = \bar{\nu}) = \nu} \\
 \hline
 \overline{X} : \tau = O(\square) \vdash \nu + E(\overline{X}' = \bar{\nu}') = \nu \\
 \frac{E'' \neq E \quad \overline{X} : \tau = O(\overline{E''}.X''') \vdash \nu + E(\overline{X}' = \bar{\nu}') = \nu''}{\overline{X} : \tau = O(\overline{E''}.X''') ++ [E''.X''] \vdash \nu + E(\overline{X}' = \bar{\nu}') = \nu''} \\
 \frac{\nu''' = O(\nu'', \nu_X) \quad \overline{X}'' = \nu_X \in \overline{X}' = \bar{\nu}'}{\overline{X} : \tau = O(\overline{E''}.X''') \vdash \nu + E(\overline{X}' = \bar{\nu}') = \nu''} \\
 \overline{X} : \tau = O(\overline{E''}.X''') ++ [E''.X''] \vdash \nu + E(\overline{X}' = \bar{\nu}') = \nu'''
 \end{array}$$

Fig. 15. Operational Semantics—Performance Layer (Event Processing)

try to apply the rules in order, the evaluation of the components of a particular rule can happen in any order. This allows the compiler to try to re-use existing information efficiently to fail fast, and makes little semantic difference so long as the expressions in the where clause do not affect the state of the program

$$\boxed{\Sigma \mid \Omega \mid \Pi \mid \bar{\rho} \vdash \alpha \rightarrow \alpha \dashv \Sigma \mid \Omega \mid \Pi \mid \bar{\rho}}$$

$$\frac{\Sigma \mid \Omega \mid \Pi \mid \bar{\rho} \vdash \bar{\sigma} \rightarrow \bar{\sigma}' \dashv \Sigma' \mid \Omega' \mid \Pi' \mid \bar{\rho}'}{\Sigma \mid \Omega \mid \Pi \mid \bar{\rho} \vdash \mathbb{A}[\bar{\sigma}] \rightarrow \mathbb{A}[\bar{\sigma}'] \dashv \Sigma' \mid \Omega' \mid \Pi' \mid \bar{\rho}'} \quad \frac{\Omega \mid \Pi \mid \bar{\rho} \vdash \alpha \rightarrow \alpha'}{\Sigma \mid \Omega \mid \Pi \mid \bar{\rho} \vdash \alpha \rightarrow \alpha' \dashv \Sigma \mid \Omega \mid \Pi \mid \bar{\rho}}$$

$$\frac{\bar{\rho} \mid \bar{B} \vdash \bar{B} \rightarrow \bar{B}' \quad \Omega = \langle \bar{T}, \bar{S}, \bar{P}, \bar{B}, \hat{A}, \hat{M} \rangle \quad \vdash \bar{\rho} \text{ not done} \quad \hat{A} = \text{arbiter} : T \{ \bar{a} \} \quad \Omega' = \langle \bar{T}, \bar{S}, \bar{P}, \bar{B}', \hat{A}, \hat{M} \rangle}{\Sigma \mid \Omega \mid \Pi \mid \bar{\rho} \vdash \langle \circ, [], \circ, \langle \bar{B}, \bar{\rho}', \bar{B}'' \rangle \rangle \rightarrow \langle \circ, \bar{a}, \circ, \langle [], [], [] \rangle \rangle \dashv \Sigma \mid \Omega' \mid \Pi \mid \bar{\rho}}$$

$$\boxed{\Omega \mid \Pi \mid \bar{\rho} \vdash \alpha \rightarrow \alpha}$$

$$\frac{}{\Omega \mid \Pi \mid \bar{\rho} \vdash \langle \{ \}, \bar{a}, \phi, \varsigma \rangle \rightarrow \langle \circ, [], \circ, \varsigma \rangle}$$

$$\frac{\phi \vdash \bar{a} \dashv \phi'}{\Omega \mid \Pi \mid \bar{\rho} \vdash \langle \circ, [], \phi, \varsigma \rangle \rightarrow \langle \circ, \bar{a}, \phi', \varsigma \rangle} \quad \frac{}{\Omega \mid \Pi \mid \bar{\rho} \vdash \langle \circ, \hat{a} \dashv \hat{a}', \phi, \varsigma \rangle \rightarrow \langle \hat{a}, \hat{a}', \phi, \varsigma \rangle}$$

$$\frac{\Omega \mid \varsigma \vdash \text{buffer group } B : T \text{ order by } o \{ \bar{S}' \} \dashv \varsigma' \quad \bar{S}' = \bar{S}'' \dashv \bar{S}''' \quad |\bar{S}''| = \min(n, |\bar{S}'|) \quad \bar{\psi} = \langle \bar{S}, [], \bar{S}'' \rangle}{\Omega \mid \Pi \mid \bar{\rho} \vdash \langle \text{choose } \bar{S} \text{ from first } n \text{ } B \{ \bar{a} \}, \bar{a}', \phi, \varsigma \rangle \rightarrow \langle \circ, [], \langle \bar{\psi}, \bar{a}, \bar{a}' \rangle; \phi, \varsigma' \rangle}$$

$$\frac{\Omega \mid \varsigma \vdash \text{buffer group } B : T \text{ order by } o \{ \bar{S}' \} \dashv \varsigma' \quad \bar{S}' = \bar{S}''' \dashv \bar{S}'' \quad |\bar{S}''| = \min(n, |\bar{S}'|) \quad \bar{\psi} = \langle \bar{S}, [], \text{reverse}(\bar{S}'') \rangle}{\Omega \mid \Pi \mid \bar{\rho} \vdash \langle \text{choose } \bar{S} \text{ from last } n \text{ } B \{ \bar{a} \}, \bar{a}', \phi, \varsigma \rangle \rightarrow \langle \circ, [], \langle \bar{\psi}, \bar{a}, \bar{a}' \rangle; \phi, \varsigma' \rangle}$$

$$\frac{}{\Omega \mid \Pi \mid \bar{\rho} \vdash \langle \text{on } [] \text{ where true } \{ \bar{\sigma} \}, \bar{a}', \phi, \varsigma \rangle \rightarrow \langle \bar{\sigma}, \bar{a}', \phi, \varsigma \rangle}$$

$$\frac{}{\Omega \mid \Pi \mid \bar{\rho} \vdash \langle \text{on } \bar{b} \text{ where false } \{ \bar{\sigma} \}, \bar{a}', \phi, \varsigma \rangle \rightarrow \langle \circ, \bar{a}', \phi, \varsigma \rangle}$$

$$\frac{\Omega \mid \bar{\rho} \mid \varsigma \mid b \vdash \langle \bar{\xi}, \bar{\sigma} \rangle \rightarrow \langle \bar{\xi}', \bar{\sigma}' \rangle \dashv \varsigma'}{\Omega \mid \Pi \mid \bar{\rho} \vdash \langle \text{on } \bar{b} \dashv b \dashv \bar{b}' \text{ where } \bar{\xi} \{ \bar{\sigma} \}, \bar{a}', \phi, \varsigma \rangle \rightarrow \langle \text{on } \bar{b} \dashv \bar{b}' \text{ where } \bar{\xi}' \{ \bar{\sigma}' \}, \bar{a}', \phi, \varsigma' \rangle}$$

$$\frac{\Pi \mid \bar{\rho} \vdash \bar{\xi} \rightarrow \bar{\xi}' \dashv \Pi}{\Omega \mid \Pi \mid \bar{\rho} \vdash \langle \text{on } \bar{b} \text{ where } \bar{\xi} \{ \bar{\sigma} \}, \bar{a}', \phi, \varsigma \rangle \rightarrow \langle \text{on } \bar{b} \text{ where } \bar{\xi}' \{ \bar{\sigma} \}, \bar{a}', \phi, \varsigma \rangle}$$

Fig. 16. Operational Semantics—Arbiter

(which the semantics requires anyway). Of these rules, the first deals with when a rule applies: all buffer match expressions must have been evaluated, and the where clause must be true. Conversely, if the where clause ever turns out to be false, the rule does not match, and we reset the redex to  $\circ$ . The penultimate rule refers to the rules for evaluating buffer match expressions belows; the key thing



$$\boxed{\phi \vdash \bar{a} \dashv \phi}$$

$$\frac{
 \langle \langle \overline{S}, \overline{S'}, \square \rangle \rangle \dashv \overline{\psi}, \bar{a}, \bar{a}'; \phi \vdash \bar{a}' \dashv \phi
 }{
 \frac{
 \psi_p = \langle S_p, \overline{S'_p}, S''_p \dashv \overline{S'''_p} \rangle \quad \psi_e = \langle S_e, \overline{S'_e}, \square \rangle \quad \psi'_e = \langle S_e, \square, \overline{S'_e} \rangle
 }{
 \overline{\psi} \dashv \psi_p \dashv \psi_e \dashv \overline{\psi'}, \bar{a}, \bar{a}'; \phi \vdash \square \dashv \langle \overline{\psi} \dashv \psi'_p \dashv \psi'_e \dashv \overline{\psi'}, \bar{a}, \bar{a}'; \phi
 }
 }{
 \frac{
 \forall \overline{S_q}. \overline{S_q} \neq \square \quad \overline{\langle S_v, \overline{S_s}, \overline{S_q} \rangle} \dashv \langle S'_v, \overline{S'_s}, S_n \dashv \overline{S'_q} \rangle \mid \square \vdash \bar{a} \rightarrow \bar{a}''
 }{
 \langle \langle \overline{S_v}, \overline{S_s}, \overline{S_q} \rangle \rangle \dashv \langle S'_v, \overline{S'_s}, S_n \dashv \overline{S'_q} \rangle, \bar{a}, \bar{a}'; \phi \vdash \bar{a}'' \dashv \langle \langle \overline{S_v}, \overline{S_s}, \overline{S_q} \rangle \rangle \dashv \langle S'_v, \overline{S'_s} \dashv S_n, \overline{S'_q} \rangle, \bar{a}, \bar{a}'; \phi
 }
 }$$

$$\boxed{\overline{\psi} \mid \overline{S} \vdash \bar{a} \rightarrow \bar{a}}$$

$$\frac{
 \overline{\square} \mid \overline{S} \vdash \bar{a} \rightarrow \bar{a} \quad \overline{\langle S_v, \overline{S_p}, \square \rangle} \dashv \overline{\psi} \mid \overline{S_s} \vdash \bar{a} \rightarrow \square \quad \frac{S_n \in \overline{S_s} \quad \langle S_v, \overline{S_p}, \overline{S'} \rangle \dashv \overline{\psi} \mid \overline{S_s} \vdash \bar{a} \rightarrow \bar{a}'}{\langle S_v, \overline{S_p}, S_n \dashv \overline{S'} \rangle \dashv \overline{\psi} \mid \overline{S_s} \vdash \bar{a} \rightarrow \bar{a}'}
 }{
 \frac{
 S_n \notin \overline{S_s} \quad \overline{\psi} \mid S_n \dashv \overline{S_s} \vdash \bar{a} \rightarrow \bar{a}'
 }{
 \langle S_v, \overline{S_p}, S_n \dashv \overline{S'} \rangle \dashv \overline{\psi} \mid \overline{S_s} \vdash \bar{a} \rightarrow \bar{a}'[S_v \mapsto S_n]
 }
 }$$

**Fig. 17.** Arbiter Rule-Matching (Backtracking)

here is that evaluating a buffer match expression may affect both the where clause and the statements to be executed when the arbiter rule applies, mainly by essentially substituting variables in matched events, but also by setting the where clause to false if a buffer match expression does not match. Finally, the arbiter allows expressions in the where clause to access the stream fields of the given streams, but not to affect the state in terms of the base language.

**Choose Expressions and Backtracking** The rules in Figure 17 deal with generating the  $k$ -permutations of event sources in buffer groups for choose expressions. An element of the choice stack  $\phi$  consists of a list of choice states  $\overline{\psi}$ , the arbiter rules  $\bar{a}$  to be tried within the current choose expression, and the arbiter rules  $\bar{a}'$  forming the continuation of the choose expression. A choice state  $\psi$  corresponds to a buffer variable  $S$  in choose expression and tracks the current state of its permutation. Essentially, the last (right-most) variable in that list makes a step every time, moving a variable from the to-be-used list on the right to the already-used list on the left. Once a variable has exhausted that list, it resets and lets the variable to its left take a step. The first rule in Figure 17 deals with the case when the first (left-most) variable has exhausted all its options: in this case, we have examined all permutations without success, so we return the continuation arbiter rules and pop the current entry from the choice stack. The second rule covers the left-propagation of making steps when all options are exhausted, while the last rule tries to generate an instantiation of

$$\boxed{\Omega \mid \bar{\rho} \mid \varsigma \mid b \vdash \langle \tilde{\xi}, \tilde{\sigma} \rangle \rightarrow \langle \tilde{\xi}, \tilde{\sigma} \rangle \dashv \varsigma}$$

$$\frac{\bar{\rho} \mid \varsigma \vdash \text{stream}(b) \mapsto \rho' \dashv \varsigma' \quad \Omega \mid \rho' \mid b \vdash \langle \tilde{\xi}, \tilde{\sigma} \rangle \rightarrow \langle \tilde{\xi}', \tilde{\sigma}' \rangle}{\Omega \mid \bar{\rho} \mid \varsigma \mid b \vdash \langle \tilde{\xi}, \tilde{\sigma} \rangle \rightarrow \langle \tilde{\xi}', \tilde{\sigma}' \rangle \dashv \varsigma'}$$

$$\boxed{\Omega \mid \rho \mid b \vdash \langle \tilde{\xi}, \tilde{\sigma} \rangle \rightarrow \langle \tilde{\xi}, \tilde{\sigma} \rangle}$$

$$\frac{\rho = \langle S, P, \Delta, [], \dot{e}, k, [] \rangle}{\Omega \mid \rho \mid S : \text{done} \vdash \langle \tilde{\xi}, \tilde{\sigma} \rangle \rightarrow \langle \tilde{\xi}, \tilde{\sigma} \rangle} \quad \frac{\rho = \langle S, P, \Delta, \Sigma, \dot{e}, k, \Sigma' \rangle \quad \Sigma \dashv \dashv \Sigma' \neq []}{\Omega \mid \rho \mid S : \text{done} \vdash \langle \tilde{\xi}, \tilde{\sigma} \rangle \rightarrow \langle \text{false}, \tilde{\sigma} \rangle}$$

$$\frac{\rho = \langle S, P, \Delta, \Sigma, \dot{e}, k, [] \rangle}{\Omega \mid \rho \mid S : \text{nothing} \vdash \langle \tilde{\xi}, \tilde{\sigma} \rangle \rightarrow \langle \tilde{\xi}, \tilde{\sigma} \rangle} \quad \frac{\rho = \langle S, P, \Delta, \Sigma, \dot{e}, k, \Sigma' \rangle \quad \Sigma' \neq []}{\Omega \mid \rho \mid S : \text{nothing} \vdash \langle \tilde{\xi}, \tilde{\sigma} \rangle \rightarrow \langle \text{false}, \tilde{\sigma} \rangle}$$

$$\frac{\rho = \langle S, P, \Delta, \Sigma, \dot{e}, k, \Sigma' \rangle \quad |\Sigma'| \geq n}{\Omega \mid \rho \mid S : n \vdash \langle \tilde{\xi}, \tilde{\sigma} \rangle \rightarrow \langle \tilde{\xi}, \tilde{\sigma} \rangle} \quad \frac{\rho = \langle S, P, \Delta, \Sigma, \dot{e}, k, \Sigma' \rangle \quad |\Sigma'| < n}{\Omega \mid \rho \mid S : n \vdash \langle \tilde{\xi}, \tilde{\sigma} \rangle \rightarrow \langle \text{false}, \tilde{\sigma} \rangle}$$

$$\frac{\rho = \langle S, P, \Delta, \Sigma, \dot{e}, k, \overline{E(\bar{\nu})} \dashv \dashv \Sigma' \rangle \quad \Omega \vdash P : T' \rightarrow T \quad \Omega \vdash \tilde{\sigma}[E(\bar{X} \mapsto \bar{\nu}) : T] \rightarrow \tilde{\sigma}'}{\Omega \mid \rho \mid S : \overline{E(\bar{X})} \vdash \langle \tilde{\xi}, \tilde{\sigma} \rangle \rightarrow \langle \tilde{\xi}[\bar{X} \mapsto \bar{\nu}], \tilde{\sigma}' \rangle} \quad \frac{\rho = \langle S, P, \Delta, \Sigma, \dot{e}, k, \overline{E'(\bar{\nu})} \dashv \dashv \Sigma' \rangle \quad |\overline{E'(\bar{\nu})} \dashv \dashv \Sigma'| < |\overline{E}| \quad \vee \quad (|\overline{E'}| = |\overline{E}| \wedge \overline{E'} \neq \overline{E})}{\Omega \mid \rho \mid S : \overline{E(\bar{X})} \vdash \langle \tilde{\xi}, \tilde{\sigma} \rangle \rightarrow \langle \text{false}, \tilde{\sigma} \rangle}$$

**Fig. 18.** Buffer Match Expression Evaluation

the current `choose` expression's arbiter rules, and makes the last variable take a step as discussed above. Because each variable has been initialized with the same list of potential event sources, this would by default generate a  $k$ -tuple, so a helper relation makes sure we skip variables that have already been used. This may end up with some variable being out of options, in which case we just generate an empty list of arbiter rules to move on to the next attempt at generating a permutation.

**Buffer Match Expressions** Figure 18 shows the rules for evaluating buffer match expressions. The top rule makes sure to either retrieve the local copy of some buffer state for the current round, or makes that copy if it does not exist. Then, for each form of buffer match expressions, there are two rules: one for the case where the expression matches the current state of the referred-to buffer, and one for the case where it does not. In the second case, we simply adjust the `where` expression of the rule to be false. Otherwise, the first three cases leave the expression and statement unchanged, while the last case substitutes the pattern-match variables in the expression and adds definition statements for them in the statement (code block).

$$\boxed{\Sigma \mid \Omega \mid \Pi \mid \bar{\rho} \vdash \tilde{\sigma} \rightarrow \tilde{\sigma} \dashv \Sigma \mid \Omega \mid \Pi \mid \bar{\rho}}$$

$$\frac{\Pi \vdash \tilde{\sigma} \rightarrow \tilde{\sigma}' \dashv \Pi'}{\Sigma \mid \Omega \mid \Pi \vdash \tilde{\sigma} \rightarrow \tilde{\sigma}' \dashv \Sigma \mid \Omega \mid \Pi'} \quad \frac{\Pi \mid \bar{\rho} \vdash \tilde{\xi} \rightarrow \tilde{\xi}' \dashv \Pi'}{\Sigma \mid \Omega \mid \Pi \mid \bar{\rho} \vdash \tilde{\xi} \rightarrow \tilde{\xi}' \dashv \Sigma \mid \Omega \mid \Pi' \mid \bar{\rho}}$$

$$\frac{\bar{\rho} = \bar{\rho}_l \dashv \langle S, P, \Delta, \Sigma, \dot{e}, k, \Sigma' \rangle \dashv \bar{\rho}_r \quad \bar{\rho}' = \bar{\rho}_l \dashv \langle S, P, \Delta[X \mapsto \nu], \Sigma, \dot{e}, k, \Sigma' \rangle \dashv \bar{\rho}_r}{\Sigma \mid \Omega \mid \Pi \mid \bar{\rho} \vdash \$S.X = \nu \rightarrow \{ \} \dashv \Sigma \mid \Omega \mid \Pi \mid \bar{\rho}'}$$

$$\frac{\bar{\rho} = \bar{\rho}_l \dashv \langle S, P, \Delta, \Sigma, \dot{e}, k, \Sigma' \dashv \Sigma'' \rangle \dashv \bar{\rho}_r \quad |\Sigma'| = n \quad \bar{\rho}' = \bar{\rho}_l \dashv \langle S, P, \Delta, \Sigma, \dot{e}, k, \Sigma'' \rangle \dashv \bar{\rho}_r}{\Sigma \mid \Omega \mid \Pi \mid \bar{\rho} \vdash \text{drop } n \text{ from } S \rightarrow \{ \} \dashv \Sigma \mid \Omega \mid \Pi \mid \bar{\rho}'}$$

$$\frac{}{\Sigma \mid \Omega \mid \Pi \mid \bar{\rho} \vdash \text{yield } E(\bar{v}) \rightarrow \{ \} \dashv \Sigma \dashv E(\bar{v}) \mid \Omega \mid \Pi \mid \bar{\rho}}$$

$$\frac{\begin{array}{l} \Omega = \langle \bar{T}, \bar{S}, \bar{P}, \bar{B}_l \dashv \hat{B} \dashv \bar{B}_r, \hat{A}, \hat{M} \rangle \\ \hat{B} = \text{buffer group } B : T \text{ order by } o \{ \bar{S}' \} \quad \bar{S}'' = \bar{S}' \cup S \\ \hat{B}' = \text{buffer group } B : T \text{ order by } o \{ \bar{S}'' \} \quad \Omega' = \langle \bar{P}, \bar{B}_l \dashv \hat{B}' \dashv \bar{B}_r, \hat{A}, \hat{M} \rangle \end{array}}{\Sigma \mid \Omega \mid \Pi \mid \bar{\rho} \vdash \text{add } S \text{ to } B \rightarrow \{ \} \dashv \Sigma \mid \Omega' \mid \Pi \mid \bar{\rho}}$$

$$\frac{\begin{array}{l} \Omega = \langle \bar{T}, \bar{S}, \bar{P}, \bar{B}_l \dashv \hat{B} \dashv \bar{B}_r, \hat{A}, \hat{M} \rangle \\ \hat{B} = \text{buffer group } B : T \text{ order by } o \{ \bar{S}' \} \quad \bar{S}'' = \bar{S}' \setminus S \\ \hat{B}' = \text{buffer group } B : T \text{ order by } o \{ \bar{S}'' \} \quad \Omega' = \langle \bar{P}, \bar{B}_l \dashv \hat{B}' \dashv \bar{B}_r, \hat{A}, \hat{M} \rangle \end{array}}{\Sigma \mid \Omega \mid \Pi \mid \bar{\rho} \vdash \text{remove } S \text{ from } B \rightarrow \{ \} \dashv \Sigma \mid \Omega' \mid \Pi \mid \bar{\rho}}$$

**Fig. 19.** Operational Semantics—Statements

$$\boxed{\Pi \mid \bar{\rho} \vdash \tilde{\xi} \rightarrow \tilde{\xi} \dashv \Pi}$$

$$\frac{\Pi \vdash \tilde{\xi} \rightarrow \tilde{\xi}' \dashv \Pi'}{\Pi \mid \bar{\rho} \vdash \tilde{\xi} \rightarrow \tilde{\xi}' \dashv \Pi'} \quad \frac{\langle S, P, \Delta, \Sigma', \dot{e}, k, \Sigma \rangle \in \bar{\rho} \quad \Delta(X) = \nu}{\Pi \mid \bar{\rho} \vdash \$S.X \rightarrow \nu}$$

**Fig. 20.** Operational Semantics—Expressions

## A.7 Statements

The semantics for statements are shown in Figure 19. The first rule refers to the semantics of statements in the base language, while the second rule allows expressions nested in a statement to affect the base language program state. The other rules are similarly straightforward: adjusting stream fields, dropping events from buffers, forwarding events to the monitor, and adjusting membership in buffer groups work just as one would expect.

## A.8 Expressions and Helper Relations

Figure 20 deals with extended expressions, simply referring to the base language semantics for most except accessing stream fields, which is straightforward.

Figures 21 and 21 define various helper relations used in other rules. The first two are used to insert variable definitions into statements for variables used in event pattern matches, the third checks whether the arbiter can expect to see any more events, and the next rules deal with updating the ordering of event sources in buffer groups by applying either a round-robin step to the appropriate buffer groups that were actually used in the last round of the arbiter trying to match any rules, and by sorting the others according to values of the selected stream field. Then, some predicates process the lookup of current stream and buffer group states if they are not already cached for a given round of checking arbiter rules, while the final three are basic lookup functions for rules used earlier.

$$\boxed{\Omega \vdash \tilde{\sigma}[E(\overline{X} \mapsto \nu) : T] \rightarrow \tilde{\sigma}}$$

$$\frac{\text{stream type } T(\Gamma) \{ \overline{E'}(\overline{\Gamma'}) \vdash E(\overline{X'} : \tau) \vdash \overline{E''}(\overline{\Gamma''}) \} \in \overline{\hat{T}}}{\langle \overline{\hat{T}}, \overline{\hat{S}}, \overline{\hat{P}}, \overline{\hat{B}}, \hat{A}, \hat{M} \rangle \vdash \tilde{\sigma}[E(\overline{X} \mapsto \nu) : T] \rightarrow \{ \tau \overline{X} = \overline{\nu} \vdash \tilde{\sigma} \}}$$

$$\boxed{\Omega \vdash P : T \rightarrow T'}$$

$$\frac{\text{stream processor } P(\Gamma) : T(\overline{\xi}) \rightarrow T'(\overline{\xi'}) \{ \overline{p}; \hat{H} \} \in \overline{\hat{P}}}{\langle \overline{\hat{T}}, \overline{\hat{S}}, \overline{\hat{P}}, \overline{\hat{B}}, \hat{A}, \hat{M} \rangle \vdash P : T \rightarrow T'}$$

$$\boxed{\vdash \overline{p} \text{ not done}}$$

$$\frac{\Sigma \vdash \Sigma' \neq \square}{\vdash \langle S, P, \Delta, \Sigma, \dot{e}, k, \Sigma' \rangle \vdash \overline{p} \text{ not done}} \quad \frac{\vdash \overline{p} \text{ not done}}{\vdash \langle S, P, \Delta, \square, \dot{e}, k, \square \rangle \vdash \overline{p} \text{ not done}}$$

$$\boxed{\overline{p} \mid \overline{B} \vdash \overline{\hat{B}} \rightarrow \overline{\hat{B}}}$$

$$\frac{\overline{p} \mid \overline{B'} \vdash \overline{\hat{B}'} \rightarrow \overline{\hat{B}''} \quad \overline{p} \mid \overline{B'} \vdash \overline{\hat{B}'} \rightarrow \overline{\hat{B}''} \quad \overline{p} \mid \overline{B'} \vdash \overline{\hat{B}'} \rightarrow \overline{\hat{B}''}}{\overline{p} \mid \overline{B} \vdash \square \rightarrow \square} \quad \frac{\overline{p} \mid \overline{B'} \vdash \overline{\hat{B}'} \rightarrow \overline{\hat{B}''} \quad \overline{p} \mid \overline{B'} \vdash \overline{\hat{B}'} \rightarrow \overline{\hat{B}''} \quad \overline{p} \mid \overline{B'} \vdash \overline{\hat{B}'} \rightarrow \overline{\hat{B}''}}{\overline{p} \mid \overline{B'} \vdash \hat{B} \vdash \overline{\hat{B}'} \rightarrow \hat{B}'' \vdash \overline{\hat{B}'''}}$$

$\hat{B} = \text{buffer group } B : T \text{ order by } o \{ \overline{S} \}$   
 $\hat{B}'' = \text{buffer group } B : T \text{ order by } o \{ \overline{S'} \}$

$$\boxed{\overline{p} \mid \overline{B} \vdash_B \overline{S} \rightarrow \overline{S}}$$

$$\frac{\overline{p} \mid \overline{B'} \vdash_B \square \rightarrow \square}{\overline{p} \mid \overline{B'} \vdash_B \overline{S} \rightarrow \overline{S'}} \quad \frac{\overline{p} \vdash^X \overline{S} \rightarrow \overline{S'}}{\overline{p} \mid \overline{B'} \vdash_B^X \overline{S} \rightarrow \overline{S'}}$$

$$\frac{B \in \overline{B'}}{\overline{p} \mid \overline{B'} \vdash_B^{\text{round robin}} S \vdash \overline{S'} \rightarrow \overline{S'} \vdash S} \quad \frac{B \notin \overline{B'}}{\overline{p} \mid \overline{B'} \vdash_B^{\text{round robin}} \overline{S} \rightarrow \overline{S}}$$

$$\boxed{\overline{p} \vdash^X \overline{S} \rightarrow \overline{S}}$$

$$\frac{\overline{p} \vdash^X \overline{S'} \rightarrow \overline{S''} \vdash \overline{S'''} \quad \forall S'' . \overline{p} \vdash^X S'' \leq S \quad \forall S''' . \overline{p} \vdash^X S \leq S'''}{\overline{p} \vdash^X S \vdash \overline{S'} \rightarrow \overline{S''} \vdash S \vdash \overline{S'''}}$$

$$\boxed{\overline{p} \vdash^X S \leq S'}$$

$$\frac{\langle S, P, \Delta, \Sigma, \dot{e}, k, \Sigma' \rangle \in \overline{p} \quad \langle S', P', \Delta', \Sigma'', \dot{e}', k', \Sigma''' \rangle \in \overline{p} \quad \Delta(X) \leq \Delta'(X)}{\overline{p} \vdash^X S \leq S'}$$

Fig. 21. Operational Semantics—Helpers, Part 1

$$\boxed{\Omega \mid \varsigma \vdash \hat{B} \dashv \varsigma}$$

$$\frac{\hat{B}' = \text{buffer group } B' : T \text{ order by } o\{\overline{S'}\} \quad \hat{B}' \in \overline{\hat{B}}}{\Omega \mid \langle \overline{B}, \overline{\rho}, \overline{\hat{B}} \rangle \vdash \hat{B}' \dashv \langle \overline{B} \cup B', \overline{\rho}, \overline{\hat{B}} \rangle}$$

$$\frac{\hat{B}'' = \text{buffer group } B' : T \text{ order by } o\{\overline{S'}\} \quad \hat{B}'' \notin \overline{\hat{B}'} \quad \hat{B}'' \in \overline{\hat{B}}}{\langle \overline{\hat{T}}, \overline{\hat{S}}, \overline{\hat{P}}, \overline{\hat{B}}, \hat{A}, \hat{M} \rangle \mid \langle \overline{B}, \overline{\rho}, \overline{\hat{B}'} \rangle \vdash \hat{B}'' \dashv \langle \overline{B} \cup B', \overline{\rho}, \overline{\hat{B}'} \dashv \hat{B}'' \rangle}$$

$$\boxed{\overline{\rho} \mid \varsigma \vdash S \mapsto \rho \dashv \varsigma}$$

$$\frac{\begin{array}{l} \varsigma = \langle \overline{B}, \overline{\rho''}, \overline{\hat{B}} \rangle \\ \rho' \notin \overline{\rho''} \quad \rho' \in \overline{\rho} \\ \rho' = \langle S, P, \Delta, \Sigma, \dot{e}, k, \Sigma' \rangle \end{array}}{\overline{\rho} \mid \varsigma \vdash S \mapsto \rho' \dashv \varsigma} \quad \frac{\rho' \notin \overline{\rho''} \quad \rho' \in \overline{\rho} \quad \rho' = \langle S, P, \Delta, \Sigma, \dot{e}, k, \Sigma' \rangle}{\overline{\rho} \mid \langle \overline{B}, \overline{\rho''}, \overline{\hat{B}} \rangle \vdash S \mapsto \rho' \dashv \langle \overline{B}, \overline{\rho''} \dashv \rho', \overline{\hat{B}} \rangle}$$

$$\boxed{\text{stream}(b) \mapsto S}$$

$b$	$\text{stream}(b)$
$S : \text{done}$	$S$
$S : \text{nothing}$	$S$
$S : n$	$S$
$S : \overline{E(\overline{X})}$	$S$

$$\boxed{\overline{\hat{E}} \vdash E : \Gamma}$$

$$\frac{E(\Gamma) \in \overline{\hat{E}}}{\overline{\hat{E}} \vdash E : \Gamma} \quad \frac{E(\Gamma) \text{ creates } T \in \overline{\hat{E}}}{\overline{\hat{E}} \vdash E : \Gamma}$$

$$\boxed{\hat{X} \vdash \nu}$$

$$\overline{\overline{X : \tau = \text{count}(\overline{E.X}) \vdash 0}} \quad \overline{\overline{X : \tau = \text{sum}(\overline{E.X}) \vdash 0}} \quad \overline{\overline{X : \tau = \text{prod}(\overline{E.X}) \vdash 1}}$$

$$\overline{\overline{X : \tau = \text{max}(\overline{E.X}) \vdash -\infty}} \quad \overline{\overline{X : \tau = \text{min}(\overline{E.X}) \vdash \infty}}$$

**Fig. 22.** Operational Semantics—Helpers, Part 2